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# Accessing the shared communication medium

**M**edia  
**A**ccess  
**C**onntrol



# Network-topologies

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- Bus
- Ring
- Star
- Tree
- grid, mesh
- fully connected

**assessment criteria:**

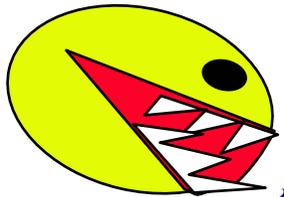
**Overhead, latency, tolerance of transmission errors and network partitions**



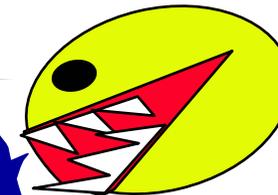
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# What are the impairments of predictability ?

**Load**



**Failures**



**Predictability**

**Network contention  
Arbitration conflicts**

**transmission errors,  
lost messages**



# Media Access Control & Logical Link Layer

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**transfer of data blocks**  
**flow control**  
**fault and error handling**  
**message re-transmissions**



# MAC-protocols

controlled access

random access

**Collision avoidance**

**Collision resolution**

Reservation-based

dynamic

static

ATM

TDMA:

TTP,  
Maruti

Token-based

Token-Ring  
Token-Bus

Timed  
Token  
Protocol

Time-based

CSMA/CA :  
Collision Avoidance

IEEE 802.11  
P-persistent CSMA

LON, VTCSMA

Master-Slave

ProfiBus DP  
FIP  
CAN-Open

Priority-based

CSMA/CA :  
Consistent Arbitration

CAN

CSMA/CD :  
Carrier Sense Multiple Access /  
Collision Detection

Ethernet

probabilistic



# **Controlled Access by Collision Exclusion:**

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## **Master/Slave**

**all control information in one place**  
**maximum of control**  
**easy to change**

## **Global Time**

**Easy temporal co-ordination**  
**Minimal communication overhead**

## **Token-based**

**Decentralized mechanism**  
**Integration of critical and non-critical messages**



# **Predictability in random access networks:**

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## **probabilistic**

**very low overhead and latency in low load conditions**  
**very flexible wrt. extensibility**  
**thrashing in high load situations**

## **Collision avoidance**

**balances the latency against the collision probability**  
**maintains a good average throughput in medium load situations**  
**may adapt to high load conditions**

## **Consistent arbitration/Collision Resolution**

**needs support from the physical layer**  
**maintains a constant throughput in all load conditions**  
**supports sophisticated fault handling**



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# CAN-Bus Controller Area Network



# CAN Milestones

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<b>1983</b>	Start of the Bosch internal project to develop an in-vehicle network
<b>1986</b>	Official introduction of CAN protocol
<b>1987</b>	First CAN controller chips from Intel and Philips Semiconductors
<b>1991</b>	Bosch's CAN specification 2.0 published
<b>1991</b>	CAN Kingdom CAN-based higher-layer protocol introduced by Kvaser
<b>1992</b>	CAN in Automation (CiA) international users and manufacturers group established
<b>1992</b>	CAN Application Layer (CAL) protocol published by CiA
<b>1992</b>	First cars from Mercedes-Benz used CAN network
<b>1993</b>	ISO 11898 standard published
<b>1994</b>	1st international CAN Conference (iCC) organized by CiA
<b>1994</b>	DeviceNet protocol introduction by Allen-Bradley
<b>1995</b>	ISO 11898 amendment (extended frame format) published
<b>1995</b>	CANopen protocol published by CiA
<b>2000</b>	Development of the time-triggered communication protocol for CAN (TTCAN)



# Requirements for the communication system

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**➔ Decentralized arbitration mechanism**

**➔ Decentralized fault handling**

**➔ Low overhead for the host processor**



# CAN in industrial automation

➔ **Technical reasons: . . . . .**

➔ **Economic reasons:**

**Component Costs**  
**Availability of Components**  
**Standardization**

Manufacturers of Fieldbus Components in Germany (Systeme/4/99)

Total	Bitbus	CAN	FIP	Interbus-S	LON	Profibus	CAN + Profibus
229	19	164	7	89	31	119	96



# The CAN Standard

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Developed by BOSCH, <http://www.semiconductors.bosch.de/pdf/can2spec.pdf>

**CAN Specification 1.2**

**CAN Specification 2.0**

**Difference between the specifications mainly is:**

- **the different length of message identifiers (CAN-ID)**

**Standard CAN:** 11 Bit IDs (defined in CAN 2.0 A ← 1.2)

**Extended CAN:** 29 Bit IDs (defined in CAN 2.0 B)

**CAN-Controller Implementations:**

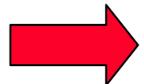
**Basic CAN:** 1 Transmit + 1 Receive (Shadow) Buffer

**Extended CAN:** 16 Configurable Transmit/Receive Buf.



# Properties of the CAN-Bus

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 **Arbitration**

**Predictable Access to the communication medium**

 **Elasticity**

**Ability to modify or extend the system without affecting already existing parts**

 **Reliability**

**Means to achieve a reliable message transfer**



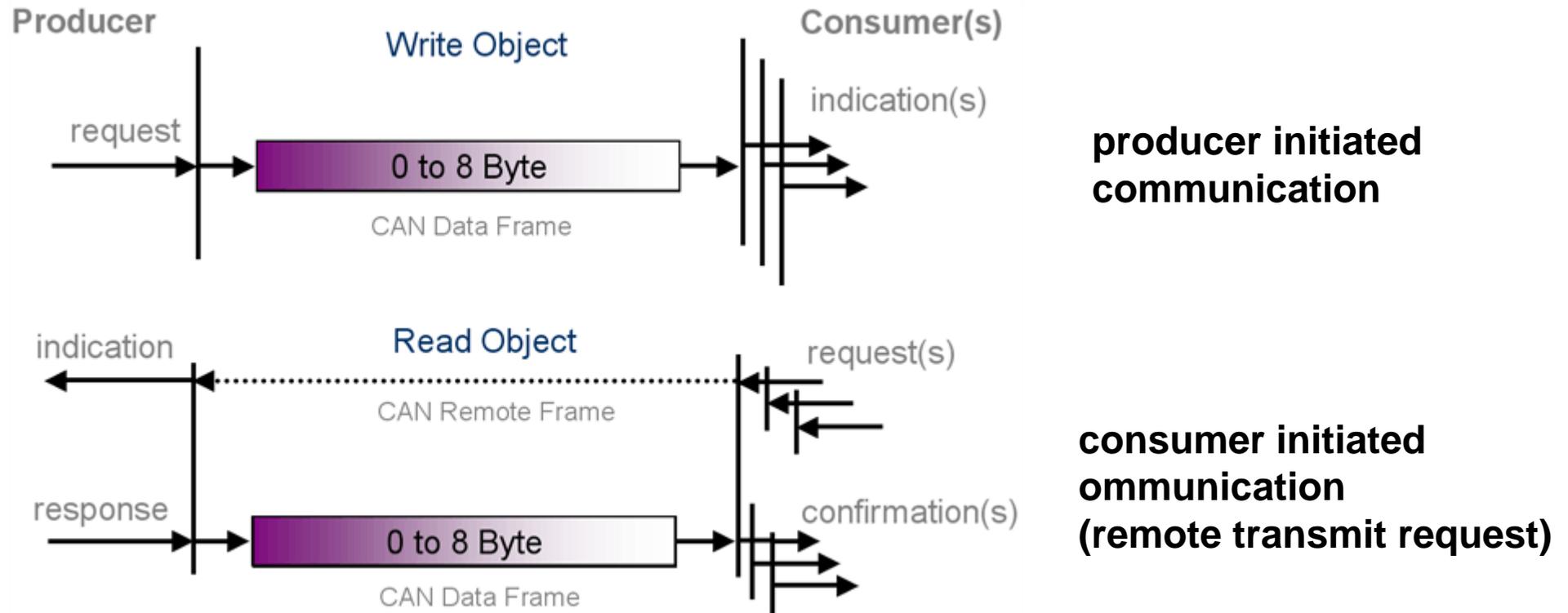
# Basic CAN properties

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- **Prioritised messages**
- **Bounded and guaranteed message delay for the highest priority message.**
- **Constant throughput in all load situations**
- **Error detection and signalling in the nodes.**
- **Automatic re-transmission.**
- **Fail silent behaviour of nodes.**
- **Consistent message delivery.**
- **Multicast with time synchronization.**



# Basic CAN bus Communication Services



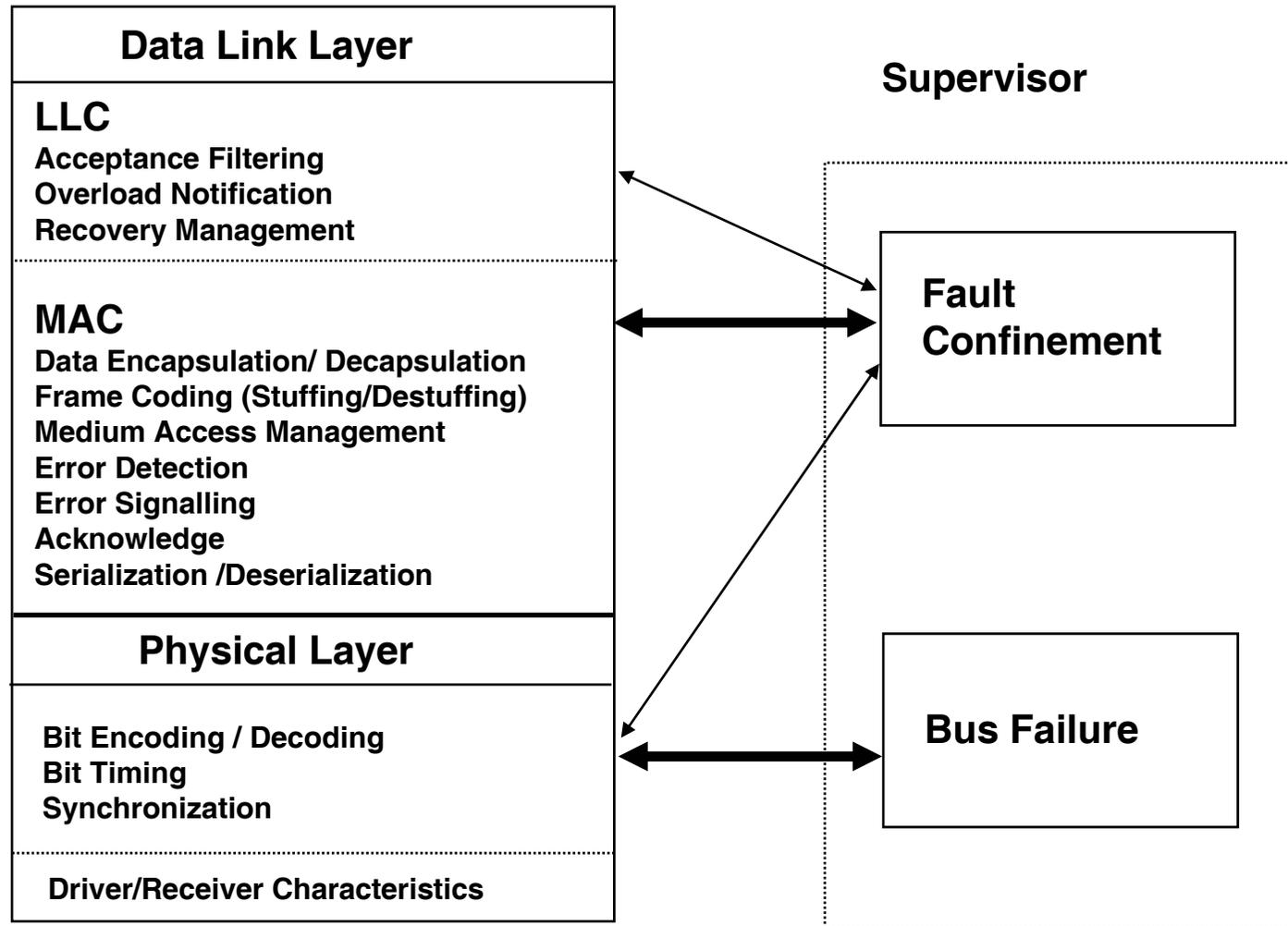
**producer initiated communication**

**consumer initiated communication (remote transmit request)**

<http://www.softing.com/home/en/industrial-automation/products/can-bus/more-can-bus/communication/>



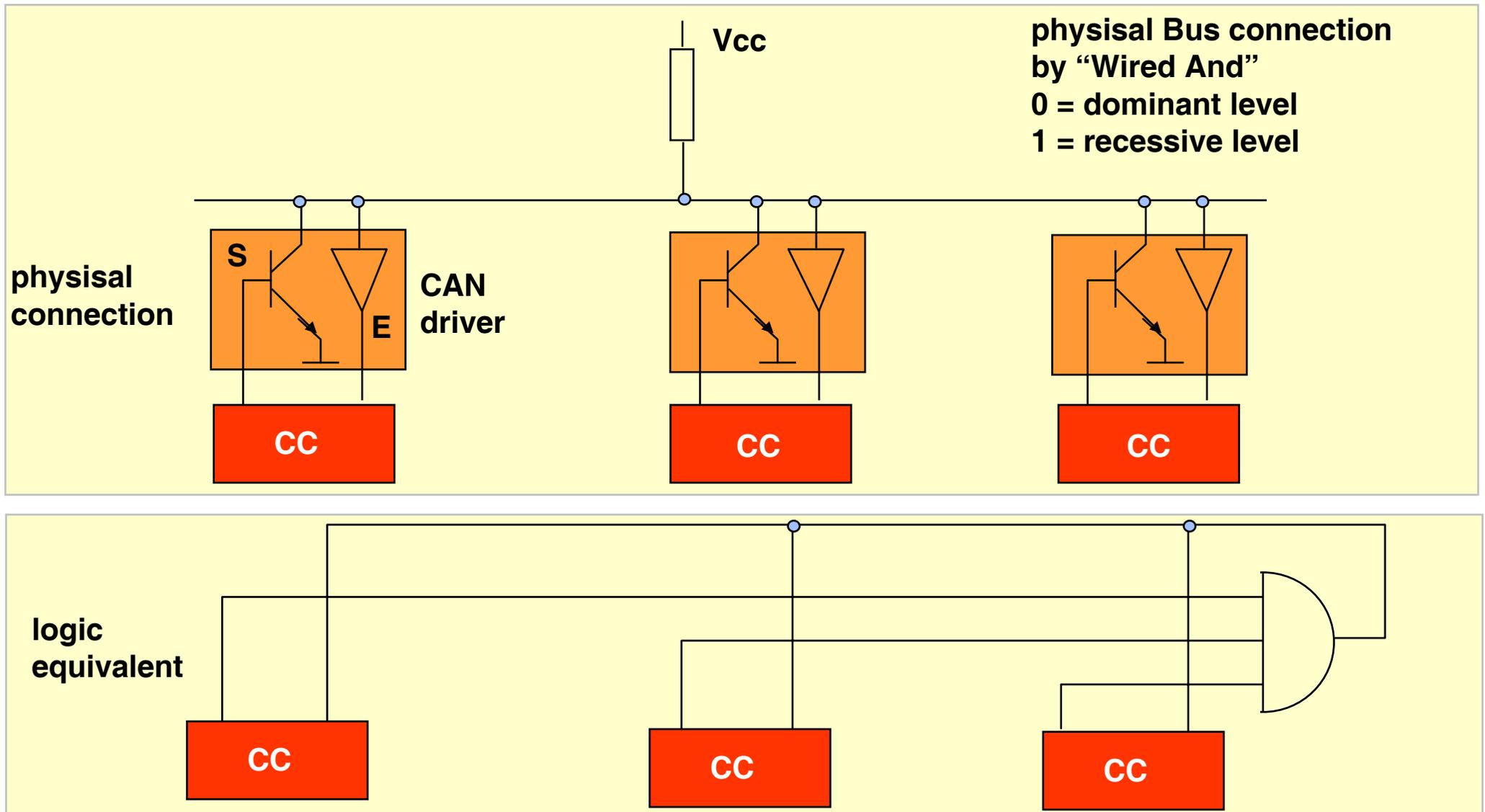
# Layers defined by the CAN standard



**LLC = Logical Link Control**  
**MAC = Medium Access Control**

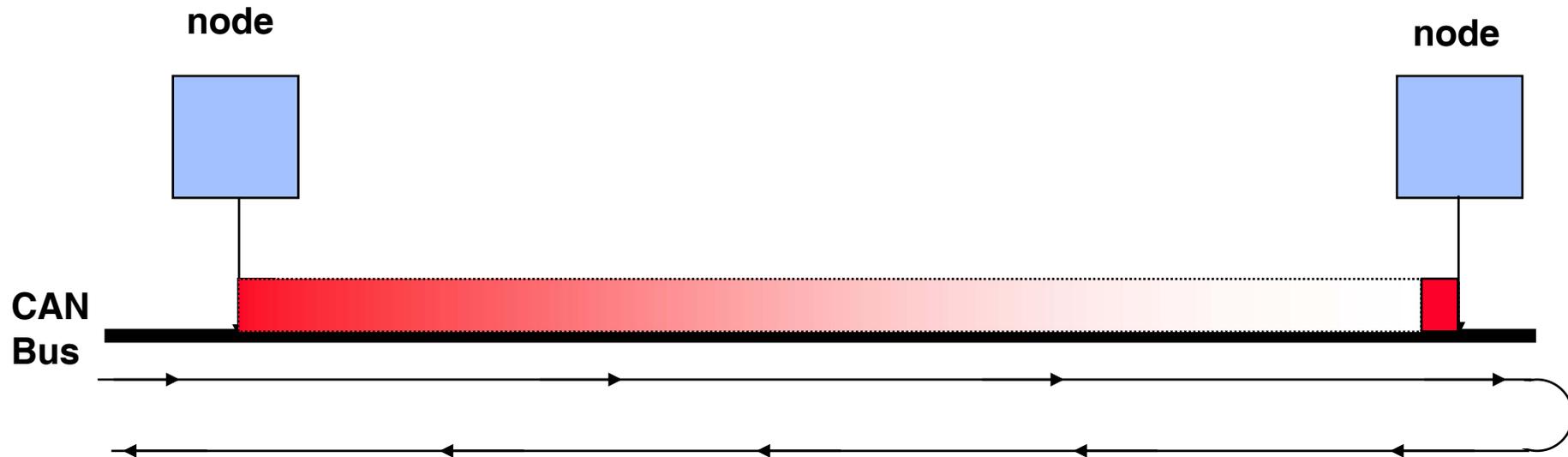


# The CAN physical layer



# CAN Bit Synchronisation

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**After a certain time, all nodes have seen the value of a bit**

**Bit rate dependend on the length of the bus**

**Bit Monitoring**



# CAN transfer rates in relation to the bus length

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$$T_d = T_{\text{TT-delay}} + T_{\text{line delay}}$$

$T_{\text{TT-delay}} \sim 100 \text{ ns}$

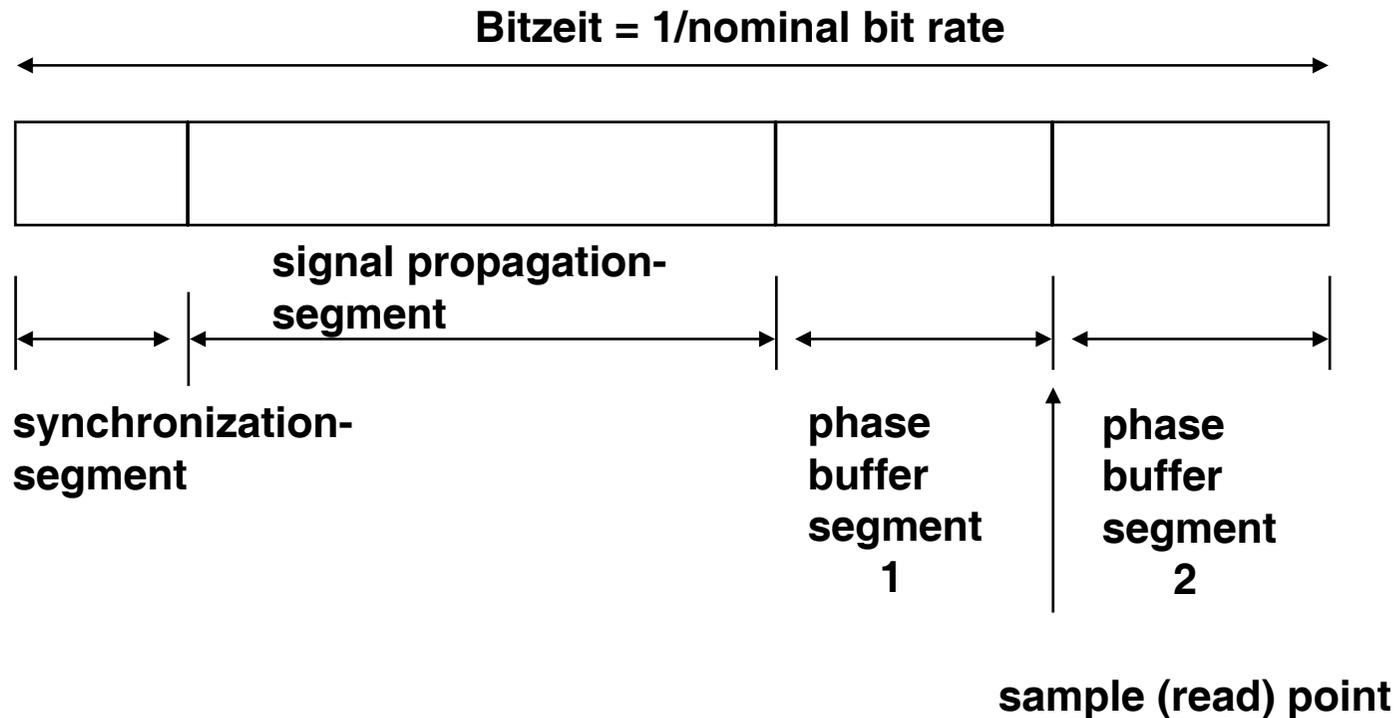
(driver, transceiver, comparator logic, etc.)

$T_{\text{line delay}} \sim 0,2 \text{ m / ns twisted pair}$

Bitrate (kBits/s)	max. network extension (m)
1000	40
500	112
300	200
200	310
100	640
50	1300



# Bit-timing and bit synchronization

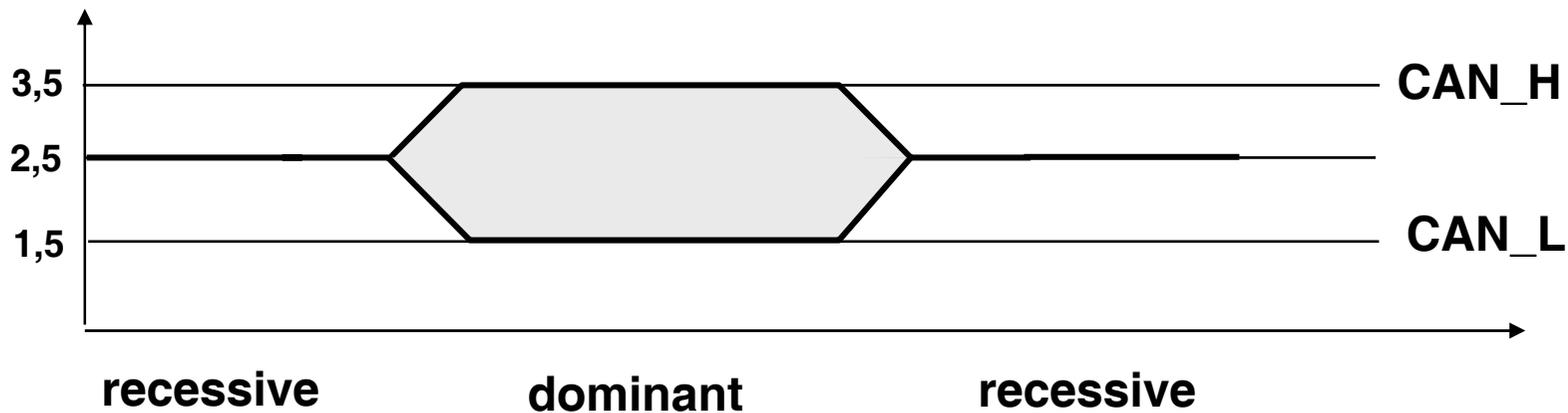
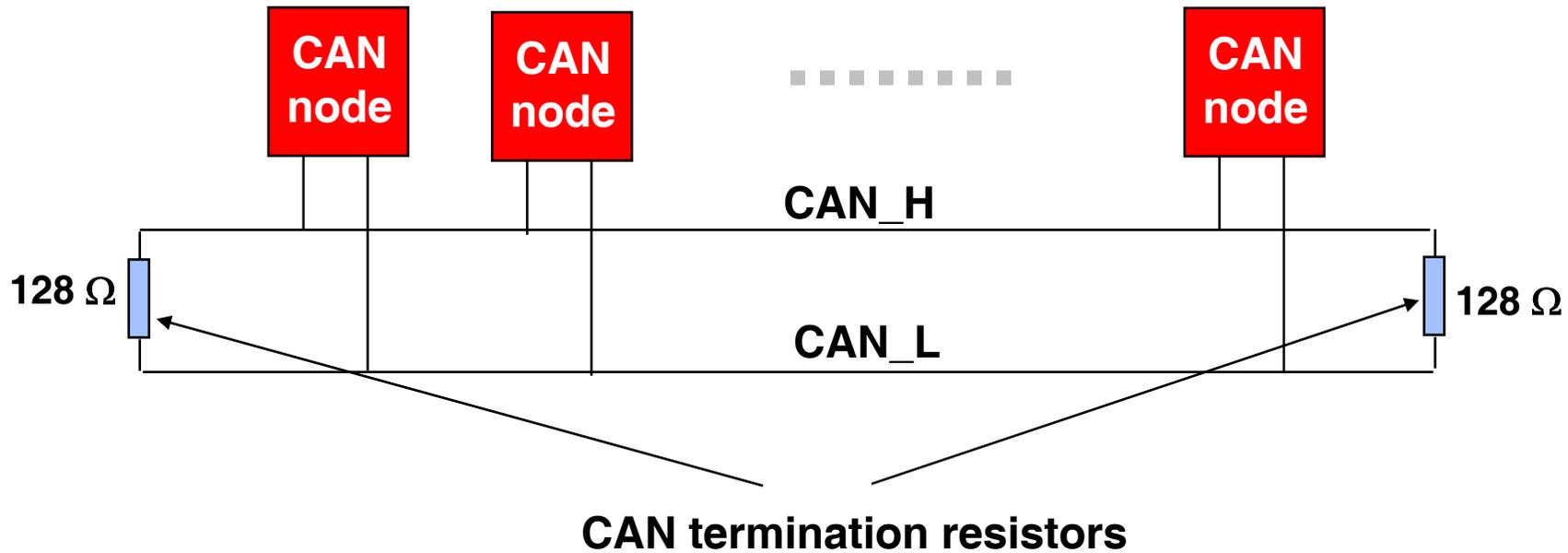


Länge der Zeitsegmente werden in Vielfachen einer aus der Oszillatorperiode abgeleiteten Zeiteinheit (time quantum) spezifiziert:

<b>synch.-segment</b>	<b>1</b>	<b>time quanta</b>
<b>sig. propag. seg.</b>	<b>1...8</b>	<b>time quantas</b>
<b>phase buffer seg. 1</b>	<b>1...8</b>	<b>time quantas</b>
<b>phase buffer seg. 2</b>	<b>1...8</b>	<b>time quantas</b>



# CAN differential transmission scheme



# The CAN MAC and Logical Link Control (LLC) levels

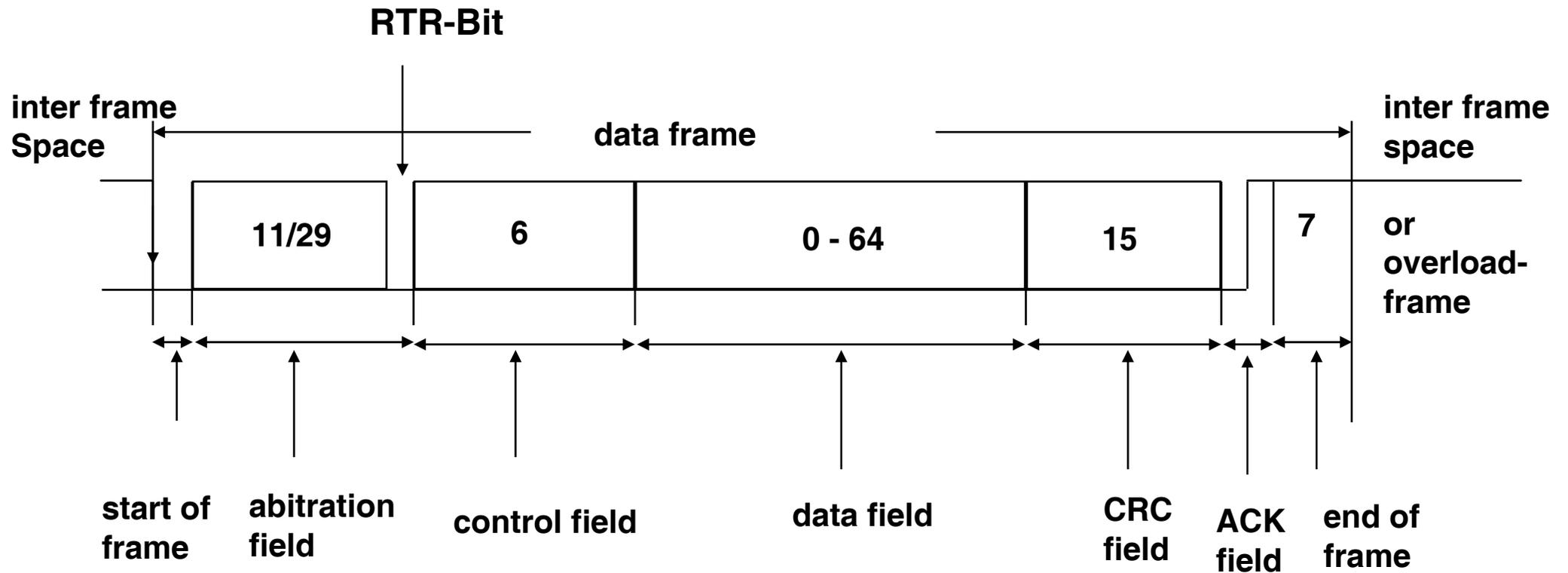
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## Frame types and formats:

- **Data Frame** normal data transmission initiated by the sender
- **Remote Frame** participant requests frame which is sent with the identical frame ID from some other participant.
- **Error Frame** participant signals an error that it has detected
- **Overload Frame** used for flow control. Results in a delayed sending of the subsequent frame.

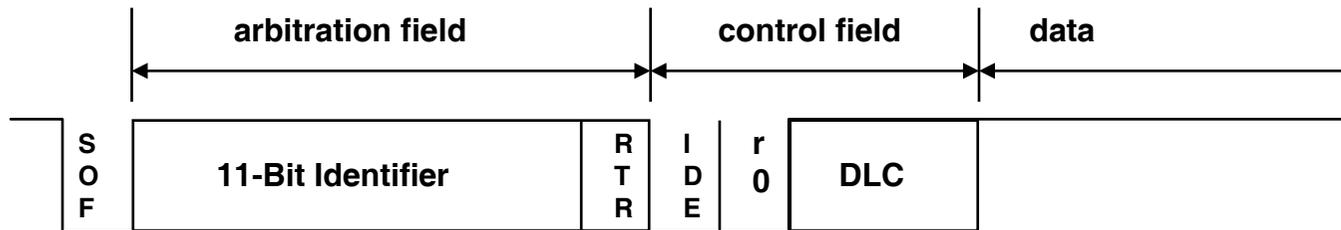


# CAN Data Frame

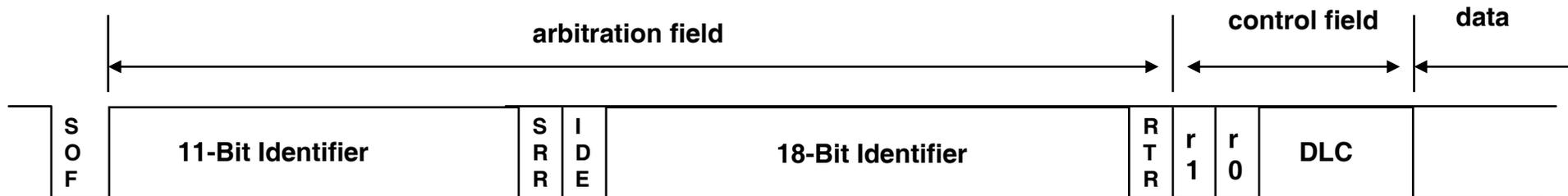


# Compatibility between standard and extended frames

## Standard Format SF (compatible to CAN Specification 1.2)



## Extended Format EF (CAN Specification 2.0)

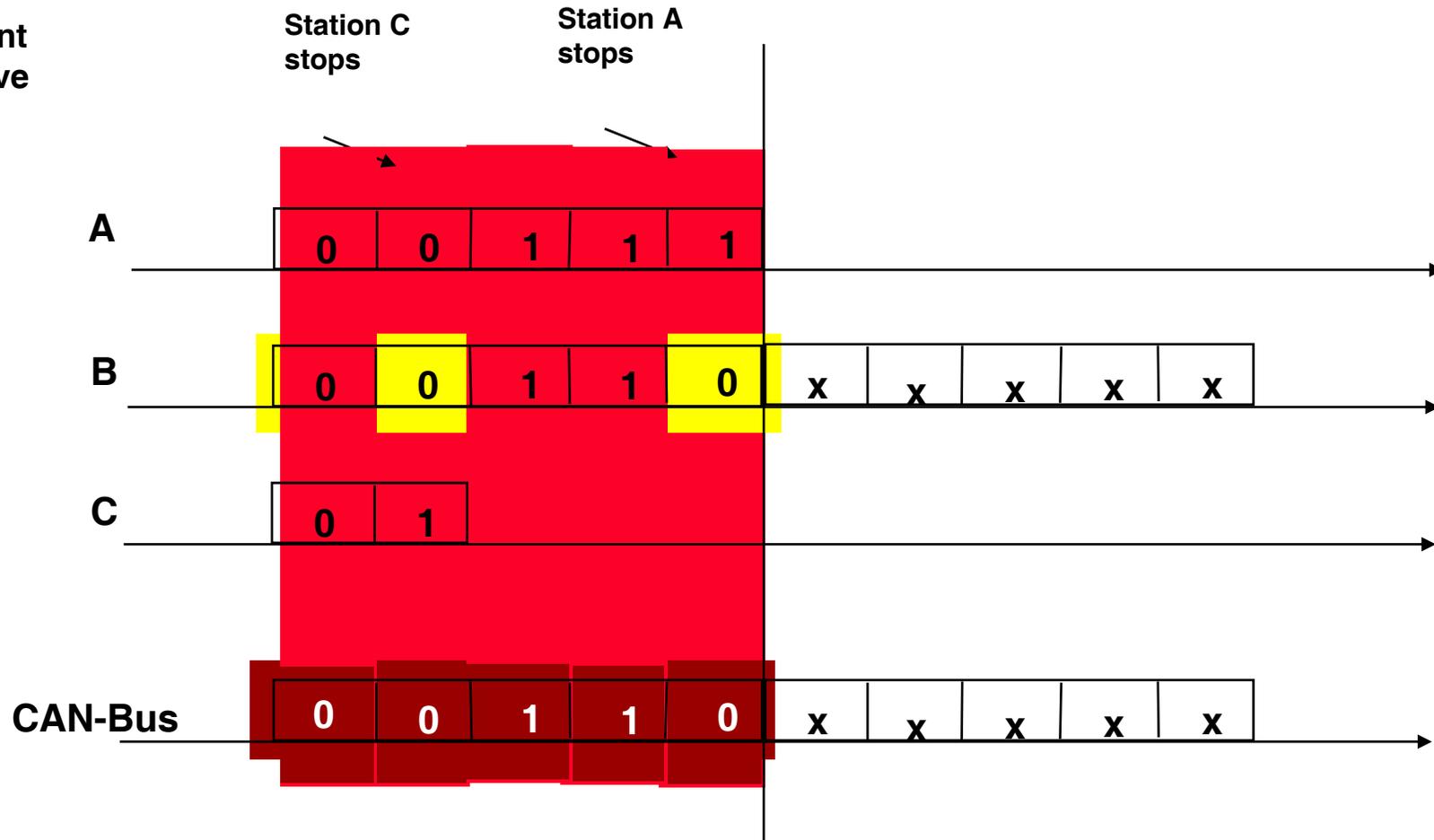


- RTR:** Remote Transmissin Request. In Data Frame: RTR = dominant. In Remote Frame: RTR = recessive. In the EF, the SSR-Bit has the funktion or the RTR-Bit
- IDE:** Identifier Extension. In the SF this is part of the control field, has a dominant value but is not interpreted. In the EF it is part of the addressing field, has a recessive value and causes the format to be recognized as EF.
- SRR:** Subsitute Remote Request. Recessive, replaces RTR in the EF for compatibility reasons.
- DLC:** Data Length Control. 0-8 Byte.
- r0, r1:** reserved



# Arbitration on a CAN-Bus

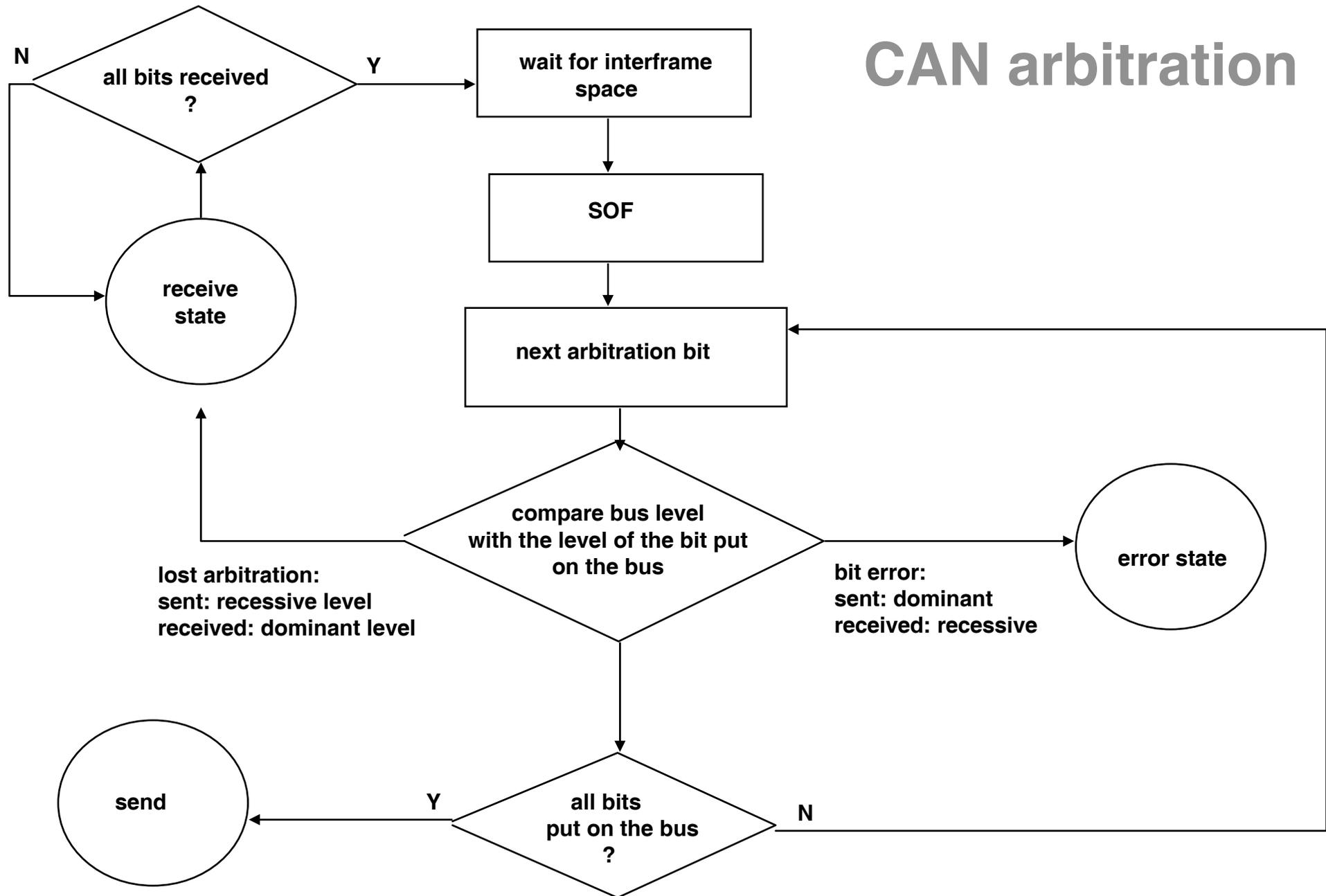
0 = dominant  
1 = recessive



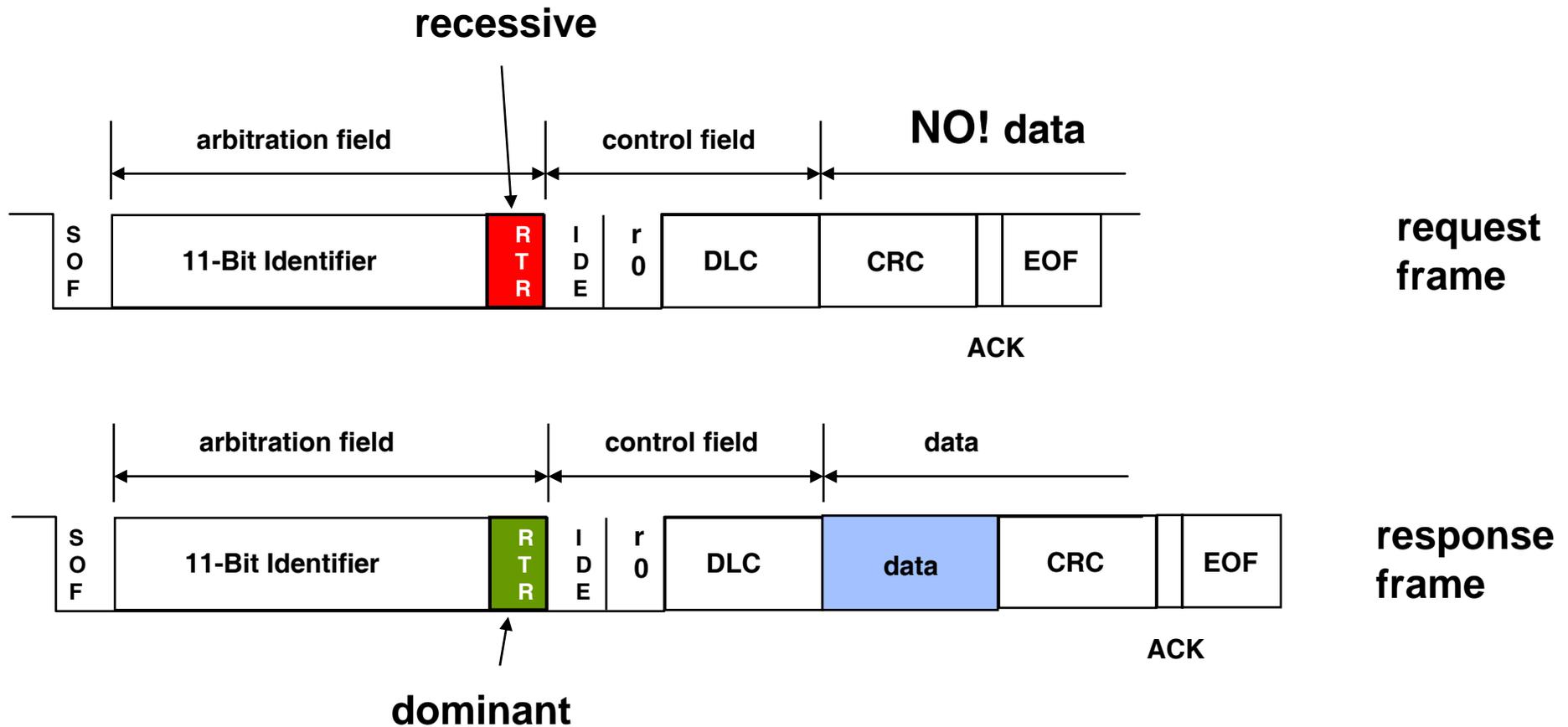
**CAN enforces a global priority-based message scheduling**



# CAN arbitration

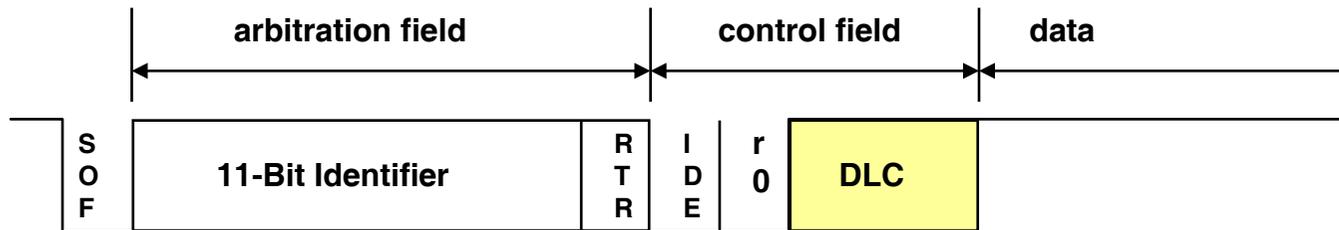


# CAN bus Remote Frame

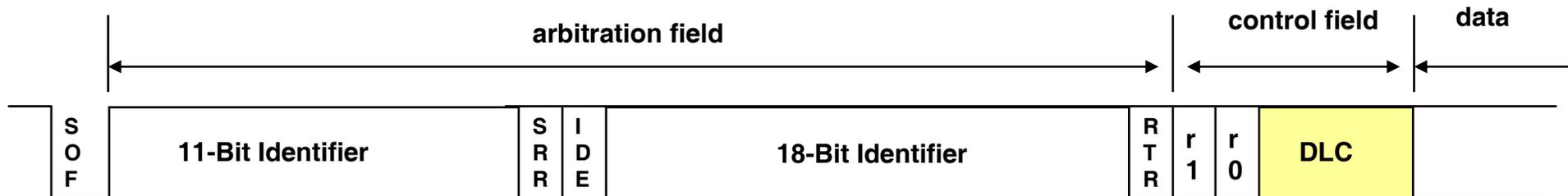


# CAN Data Frame

## Standard Format SF (compatible to CAN Specification 1.2)

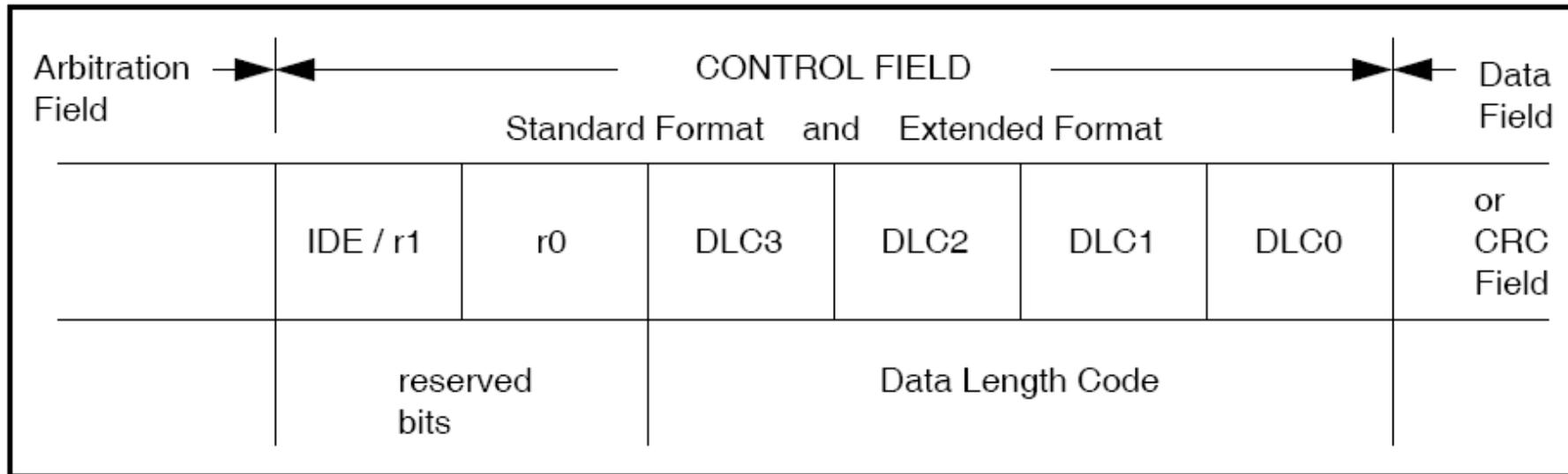


## Extended Format EF (CAN Specification 2.0)

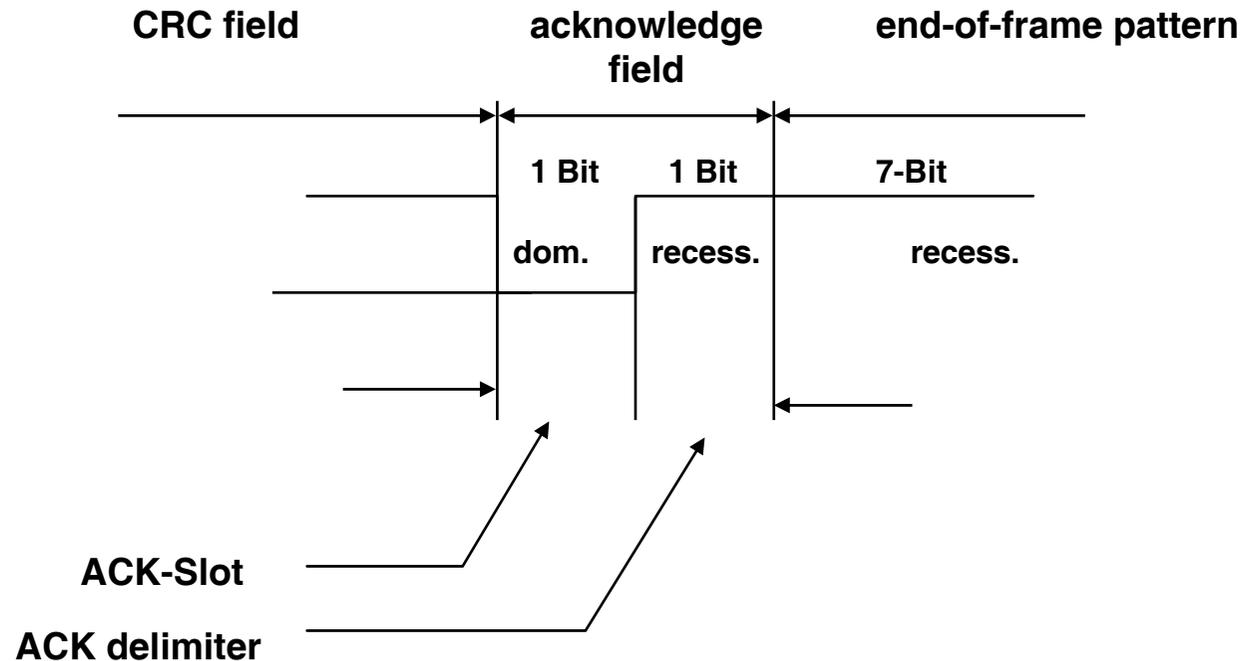


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- IDE:** Identifier Extension. In the SF this is part of the control field, has a dominant value but is not interpreted. In the EF it is part of the addressing field, has a recessive value and causes the format to be recognized as EF.
- SRR:** Subsitute Remote Request. Recessive, replaces RTR in the EF for compatibility reasons.
- DLC:** Data Length Control. 0-8 Byte.
- r0, r1:** reserved





# Anonymous acknowledgement of a CAN message



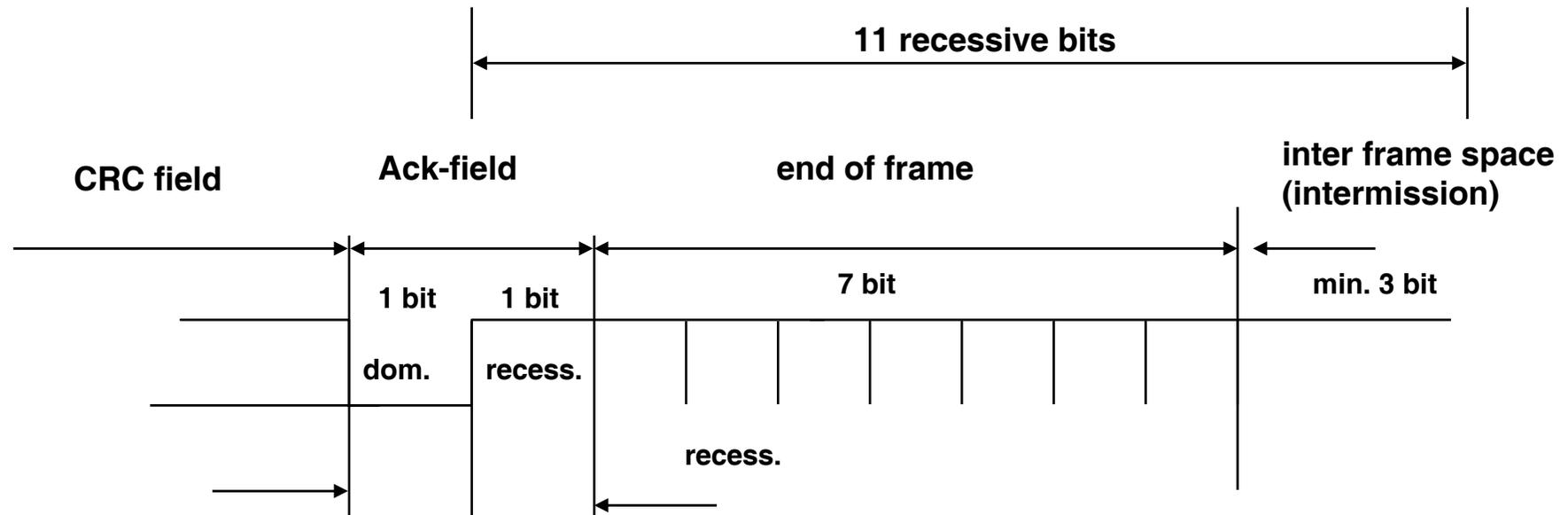
## positive anonymous acknowledgement (Broadcast !)

receivers that correctly received a message(a matching CRC sequence) report this in the ack-slot by superscibing the recessive bit of the sender by a dominat bit. The sender switches to a recessive level.

- ➡ Message is acknowledged by a single correct reception on a correct node.
- ➡ Systemwide data consistency requires additional signalling of local faults.



# Termination sequence of a frame



## Goals:

1. Detecting AND signalling the error within the actual frame in which it occurred
2. Identifying the node which may have caused the error.
3. Creating a systemwide view on the reception state of the message.

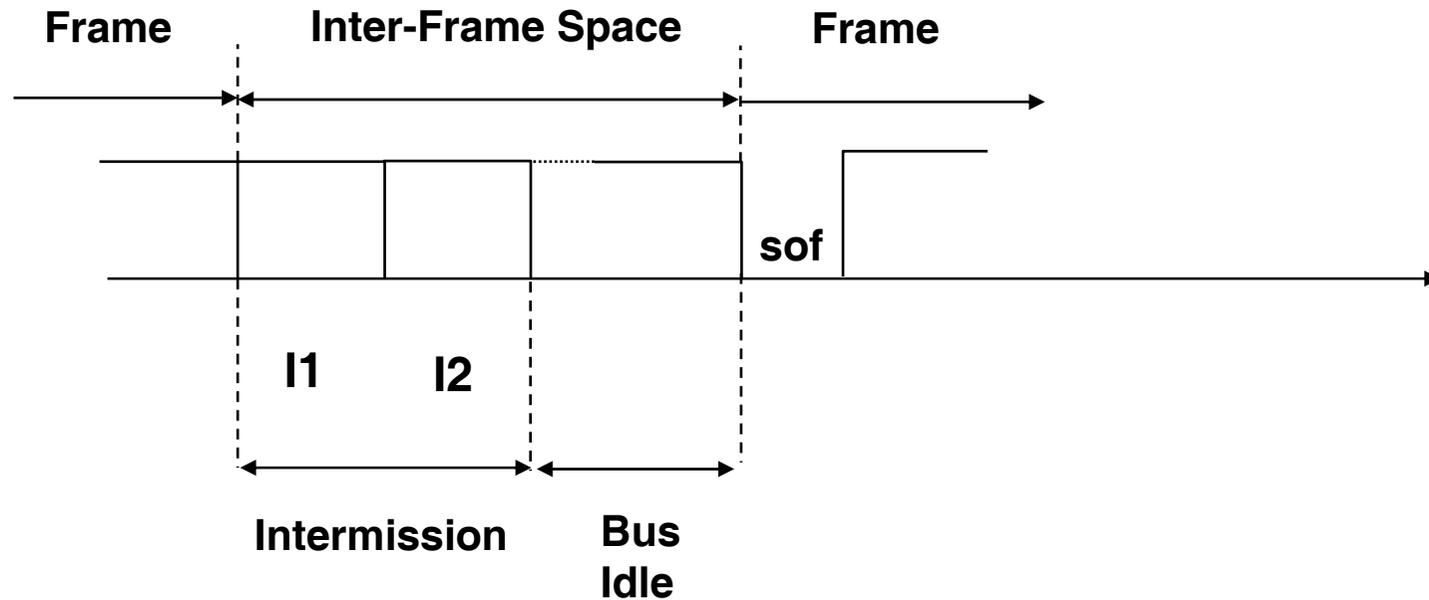
**Approach:** End of frame pattern consisting of 7 recessive bits.

1. Any error detection is signalled by putting a dominant bit on the bus.
2. An out-of-sync node, not being aware of the EOF sequence will signal an error at position "6".



# Interframe Space

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**Intermission: no data- or remote Frame may be started**

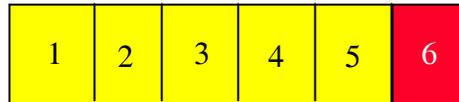
**Intermission 1: active overload Frame may be started**

**Intermission 2: re-active overload frame (after detecting a dominant bit in I1)**



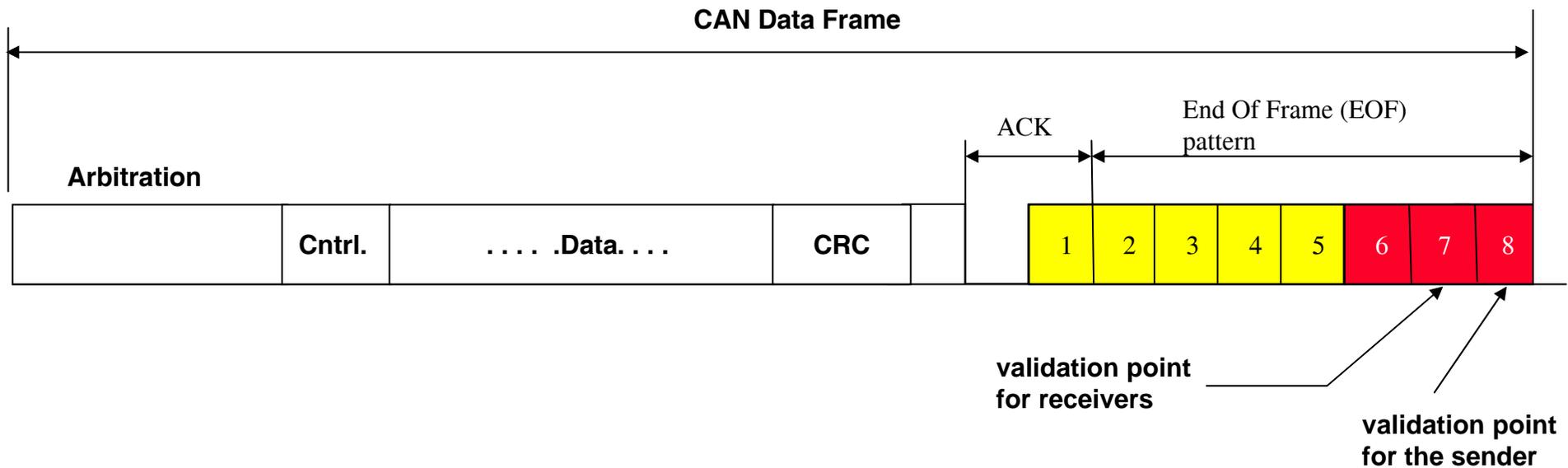
# Error Detection and Error Signalling in CAN

Violation of the Bit-Stuffing Rule:  
Used for Error Detection and Signalling



Bit-Stuffing enforces the following rule:

A sequence of 5 identical bit levels  
is followed by a complementary bit level



# Error detection

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1.) **Monitoring: Sender compares the bit sent with the bit actually on the bus.**

Type of faults: local sender faults

Error detection: sender based

2.) **Cyclic Redundancy Check:**

Type of faults: 5 arbitrarily distributed faults in the code word,  
burst error max. length 15.

Error detection: receiver based

3.) **Bitstuffing:**

Type of faults: transient faults, stuck-at-faults in the sender

Error detection: receiver based

4.) **Format control:**

Type of faults: the specified sequence of fields is violated.

Error detection: receiver based

5.) **Acknowledgment:**

Type of faults: no acknowledge

Error detection: sender based, sender assumes local fault.



# Risk of undetected errors

**Bit monitoring:** An error will not be detected if

- the sender is correct and monitoring doesn't detect an error
- all other nodes receive the same bit pattern which is different from that of the sender and contains a non-detectable error.

**CRC:** difference between frame sent and received is a multiple of the generator polynome.

**Frame errors:** the frame is shortened or additional bits are added. Ath the same time a corect end-of-frame sequence is generated.

Unruh, Mathony und Kaiser: "Error Detection Analysis of Automotive Communication Protocols", SAE International Congress, Nr. 900699, Detroit, USA, 1990

Scenario:

nodes: 10, Bit error rate:  $2 \cdot 10^{-2}$ , message error rate:  $10^{-3}$

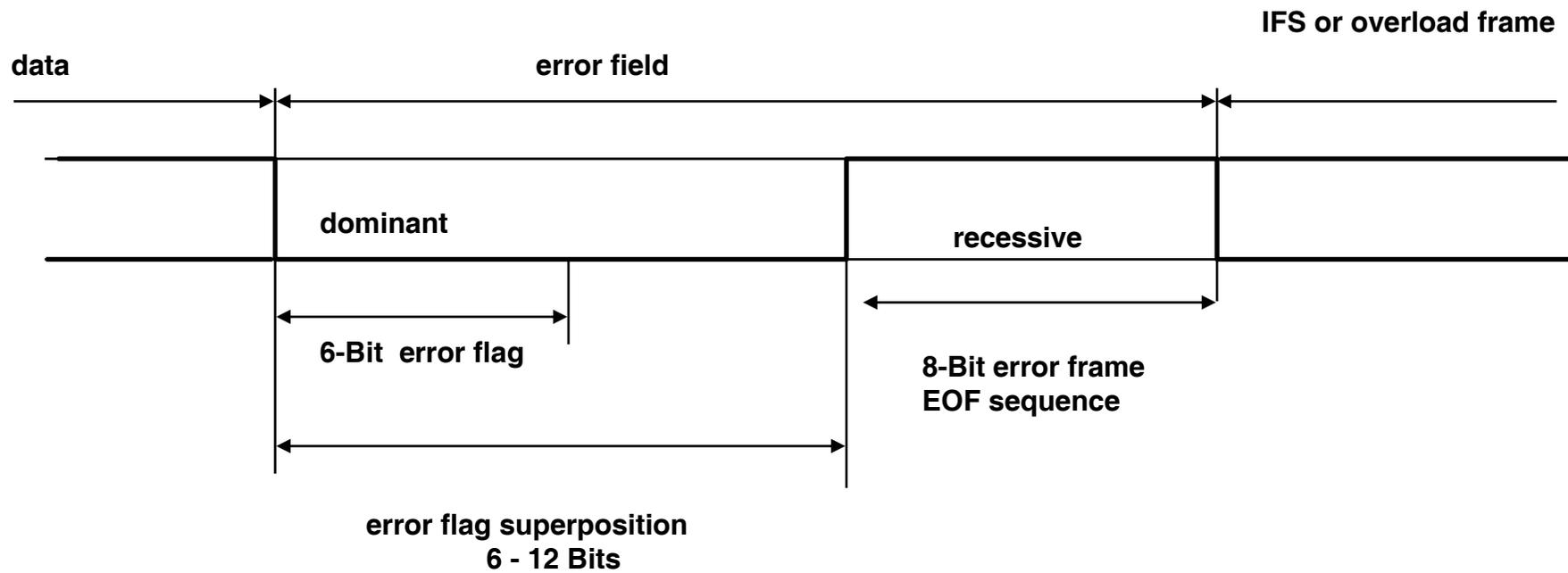
risk of undetected errors:  $4,7 \cdot 10^{-14}$

When the number of nodes increase, the probability of undetected errors decreases.

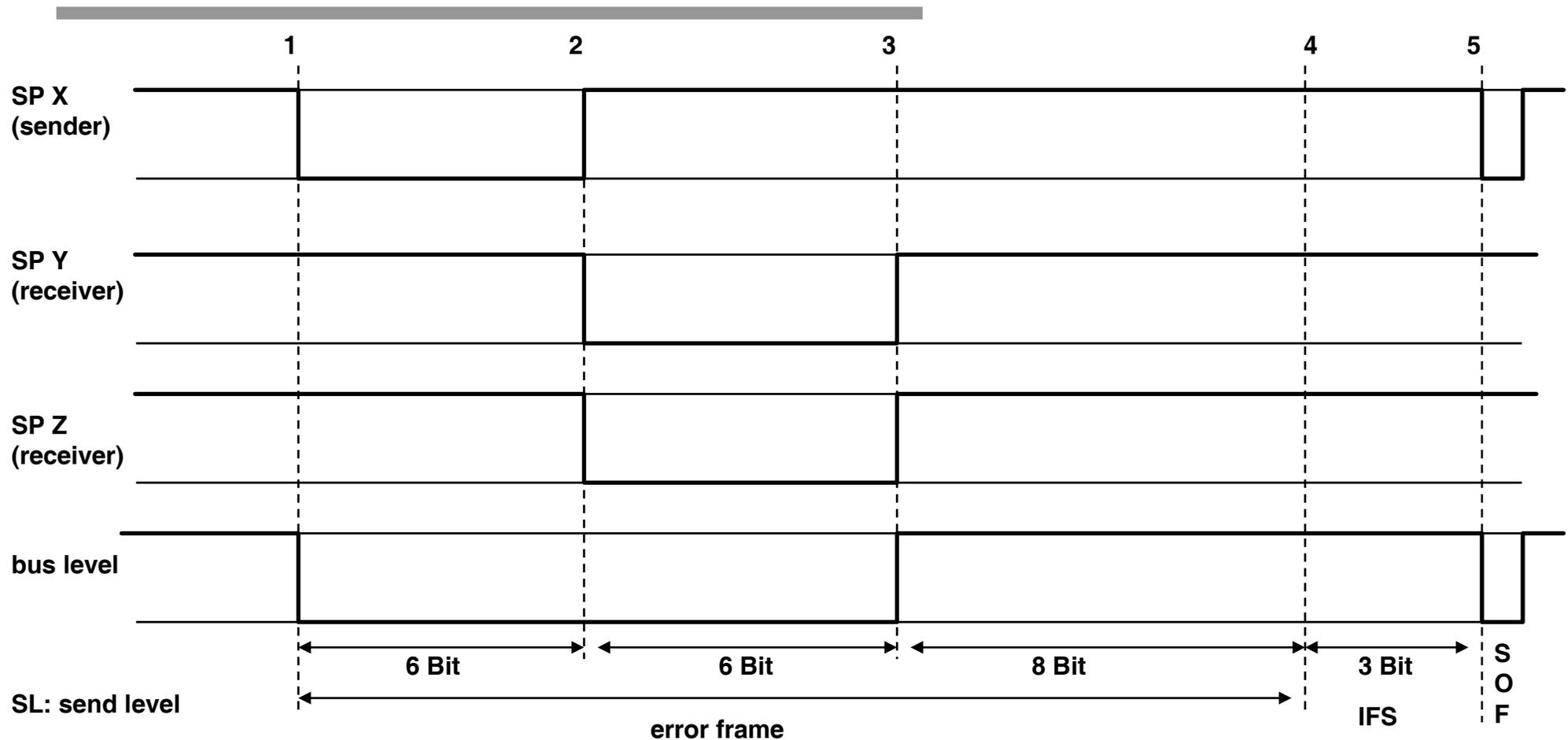


# CAN error frame

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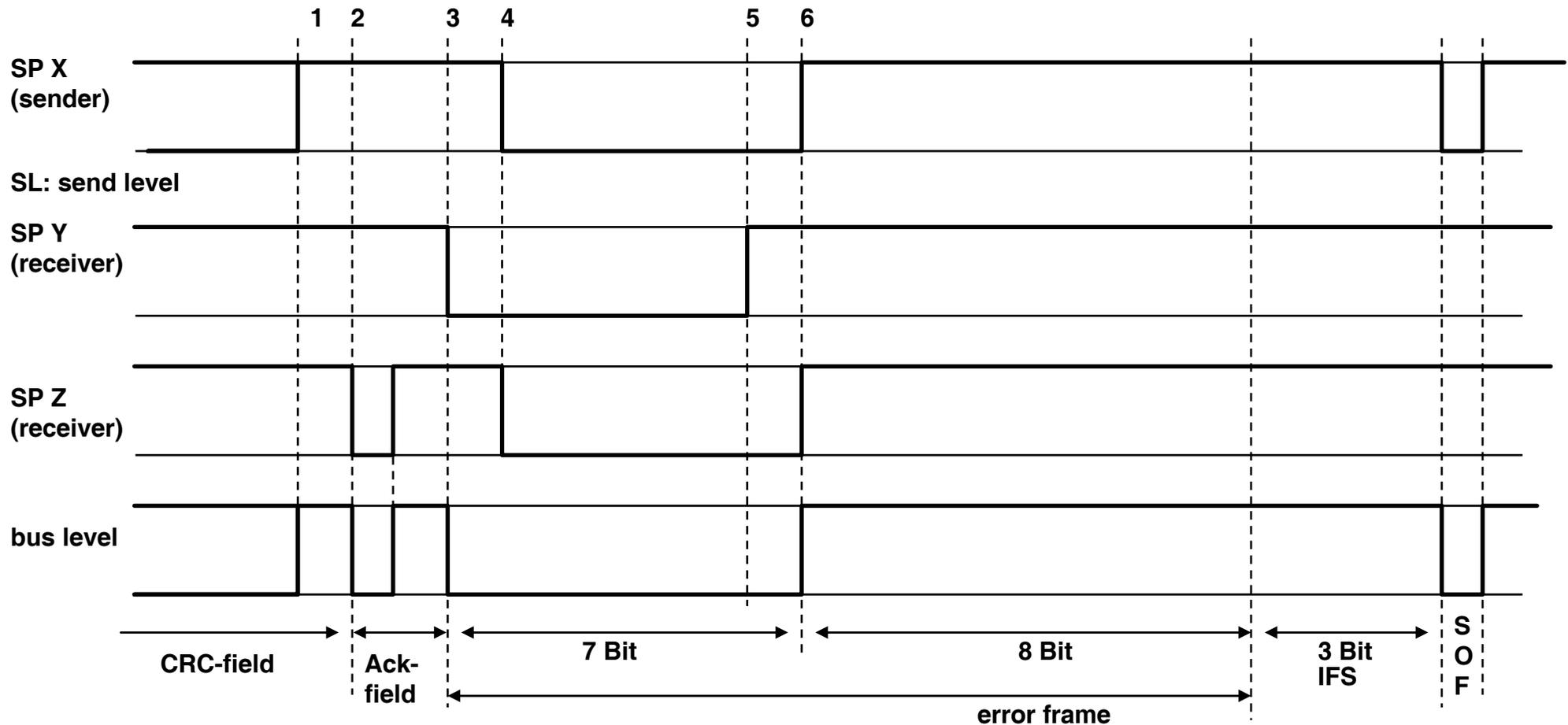
# Error frame resulting from a sender fault



time to re-transmit a faulty message frame: min. error recovery time: 23 bit times



# Error frame resulting from a receiver fault



time to re-transmit: min. error recovery time: 20 bit times



# **Enforcing fault confinement and a “Fail Silent” behaviour**

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**Problem:** Faulty component may block the entire message transfer on the CAN-Bus.

**Assumption:**

- 1. A faulty node detects the error first.**
- 2. frequently being the first which detects an error --> local fault in the node**

**approach:** error counter for receive and transmit errors. If error was first detected by the node, the counter is increased by 8-9.

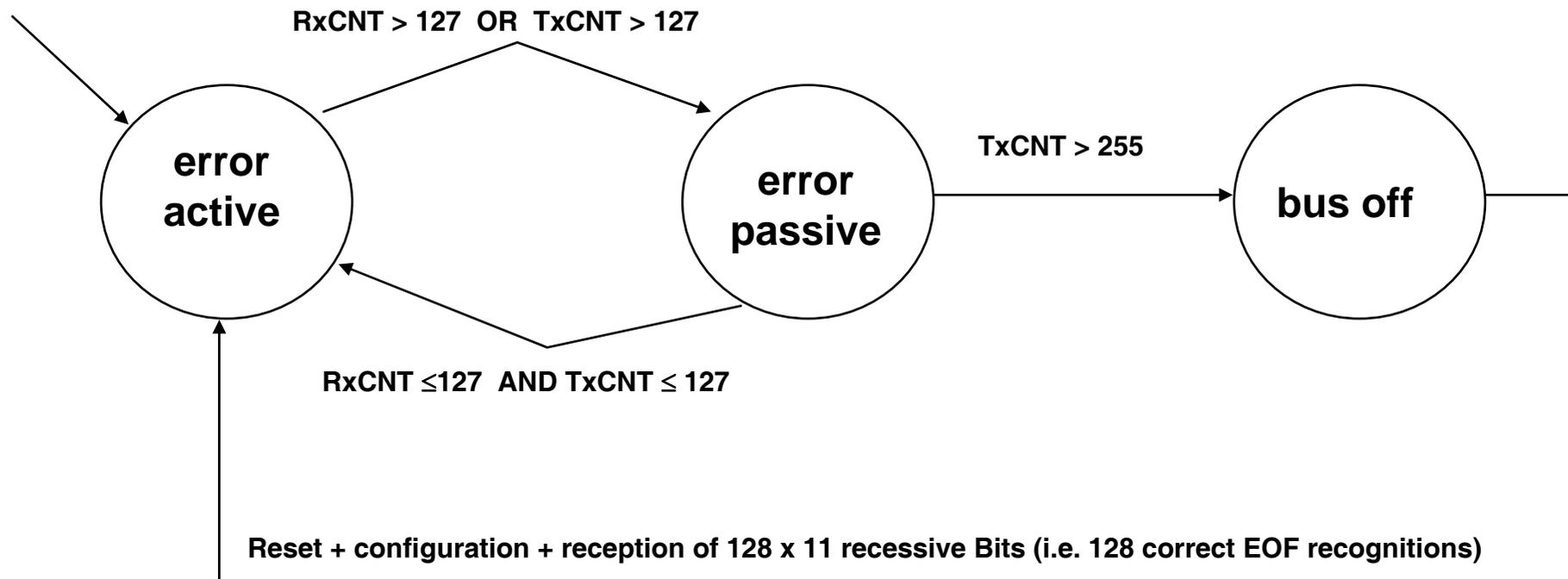


# Enforcing fault confinement and a “Fail Silent” behaviour

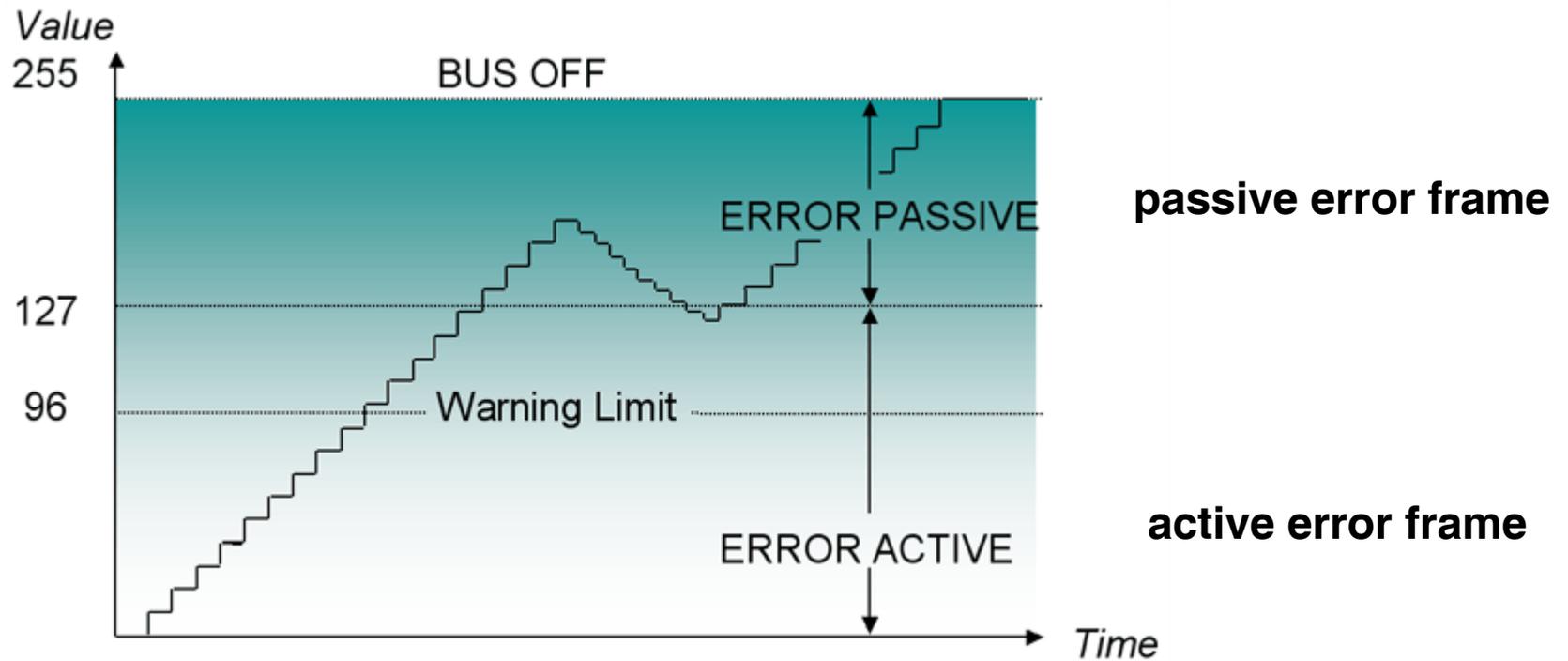
States of a CAN node:

- error active
- error passive
- bus off

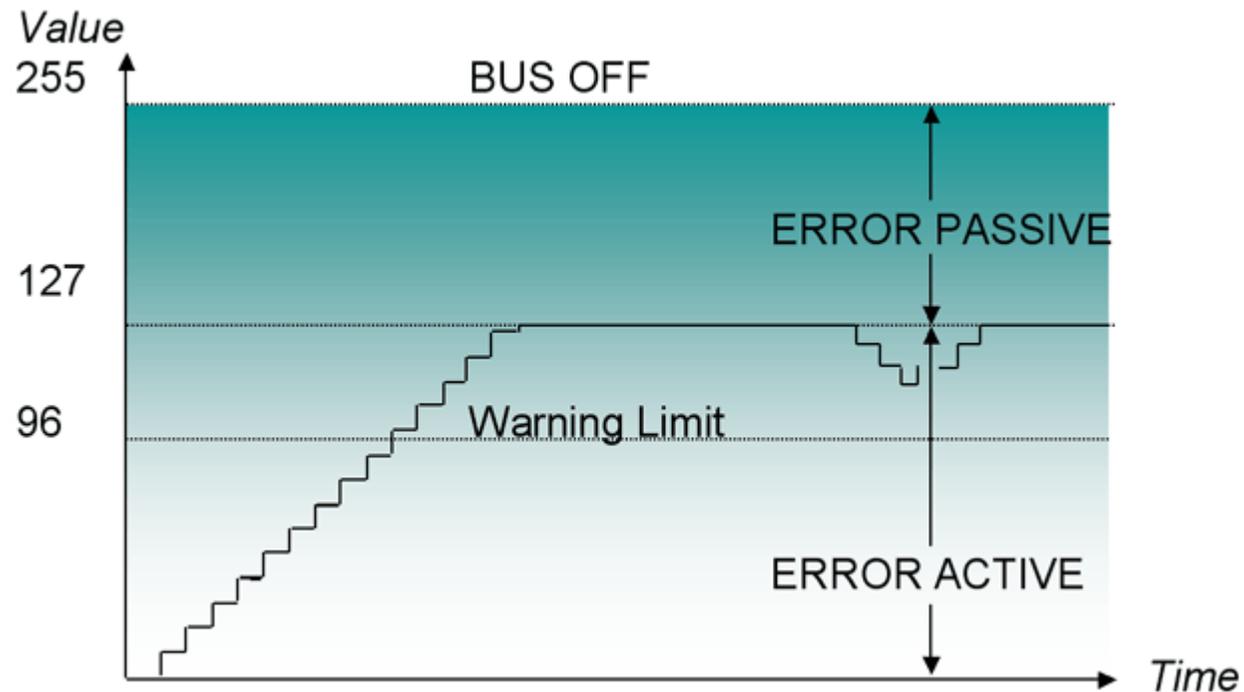
RxCNT: Value of the receive counter  
TxCNT: Value of the transmit counter



# CAN bus Error Handling - Transmit Error Counter

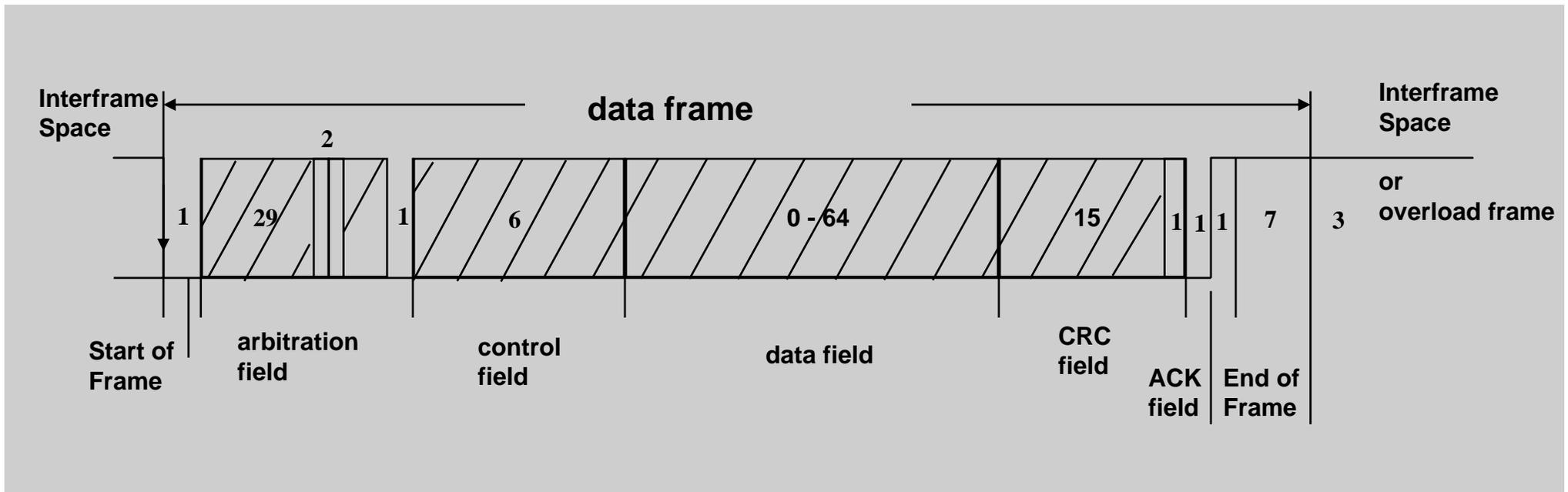


# CAN bus Error Handling - Receive Error Counter



# Analysis of CAN inaccessibility

## CAN Data Frame



### longest possible message:

Format-Overhead: 67 bit times

Data: 64 bit times

Bitstuffing (max): 23 bit times

**total: 154 bit times**



# CAN Inaccessibility Times\*

Data Rate 1 Mbps , Standard Format

Scenario	$t_{inacc}$ ( $\mu$ s)	
Bit Errors	155.0	← worst case
Bit Stuffing Errors	145.0	single
CRC Errors	148.0	
Form Errors	154.0	
Ack. Errors	147.0	
Overload Errors	40.0	
Reactive Overload Errors	23.0	
Overload Form Errors	60.0	
Transmitter Failure	2480.0	← worst case
Receiver Failure	2325.0	multiple

P. Verissimo, J. Ruffino, L. Ming:” How hard is hard real-time communication on field-busses?”



## Predictability of various Networks\*

Worst Case Times of Inaccessibility*	$t_{inacc}$ (ms)	
ISO 8002/4 Token Bus (5 Mbps)	139.99	Token-based Protocols
ISO 8002/5 Token Ring (4 Mbps)	28278.30	
ISO 9314 FDDI (100 Mbps)	9457.33	
Profibus (500 kbps)	74.80	
CSMA/CD	unbounded stochastic	CSMA Protocols
CSMA/CA		
CAN-Bus (1Mbps)	2.48	

The worst-case-delay of the Timed-Token-Protocol\*\* is  $2 \cdot TTRT$  (Target Token Rotating Time)

\* P. Verissimo, J. Ruffino, L. Ming: "How hard is hard real-time communication on field-busses?"

\*\* J.N. Ulm: "A Timed Token Ring Local Area Network and its Performance Characteristics"

R.M. Grow: "A Timed Token Protocol for local Area Networks"



# CAN-Bus Properties (summary)

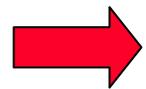
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- ➔ **Event-triggered communication with low latency**
- ➔ **Priority-based arbitration with collision resolution for guaranteed throughput**
- ➔ **error handling:**
  - anonymous positive acknowledge**
  - negative ack. in case of an error (systemwide messaging)**
  - identification of faulty nodes**
  - immediate synchronisation and retransmission**
- ➔ **content-based addressing with a high flexibility (system elasticity)**



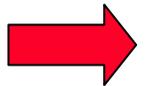
# Elasticity Mechanisms

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## **Anonymous Communication**

**Every message is broadcasted to every station**



## **Acceptance filtering on the receiver side**

**The arbitration field is used to identify a message,  
not a source or destination address**

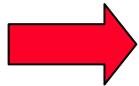


## **Content based message tagging**



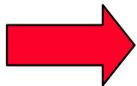
# Reliability of message transfer

## **Decentralized mechanism for consistent error handling**



### **System consistency**

**All nodes have the same view about the status of a message**



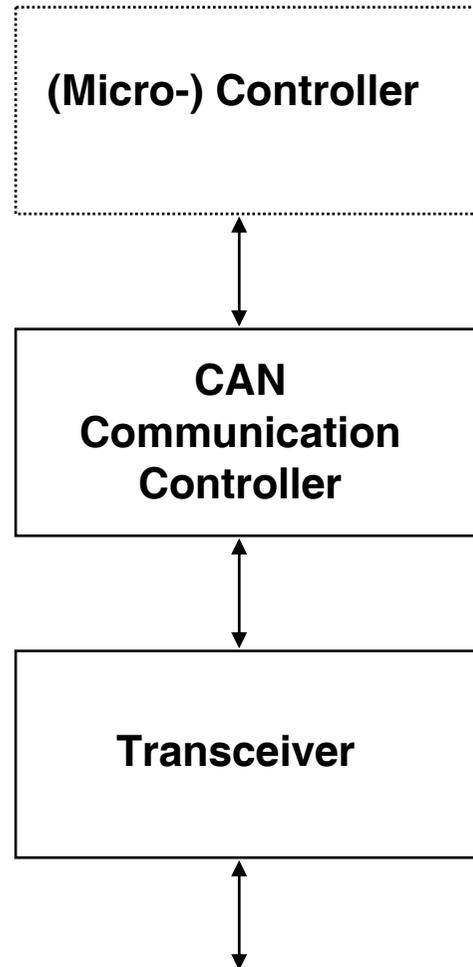
### **Fault Confinement**

**Preventing contention of network due to faulty network controller**



# CAN components

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- Stand-alone
- Microcontroller + CAN- controller
- I/O-componets (SLIO)
- Transceiver components

Full CAN components  
Basic CAN components



# Tasks of a CAN Controller

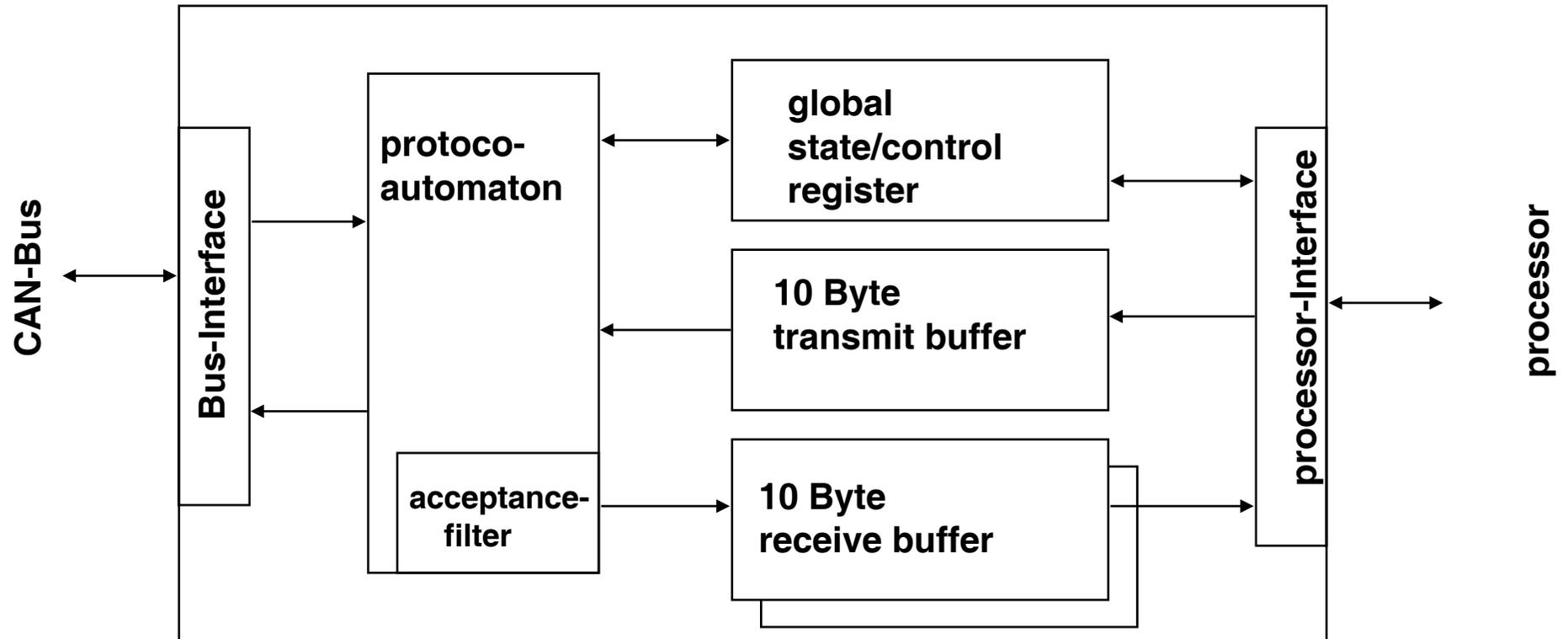
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- **bus arbitration**
- **assembling and de-assembling CAN frames**
- **generating and checking of the CRC**
- **error detection and signalling**
- **inserting and deleting stuff bits**
- **generating and testing the acknowledge pattern**
- **synchronizing the bit stream**

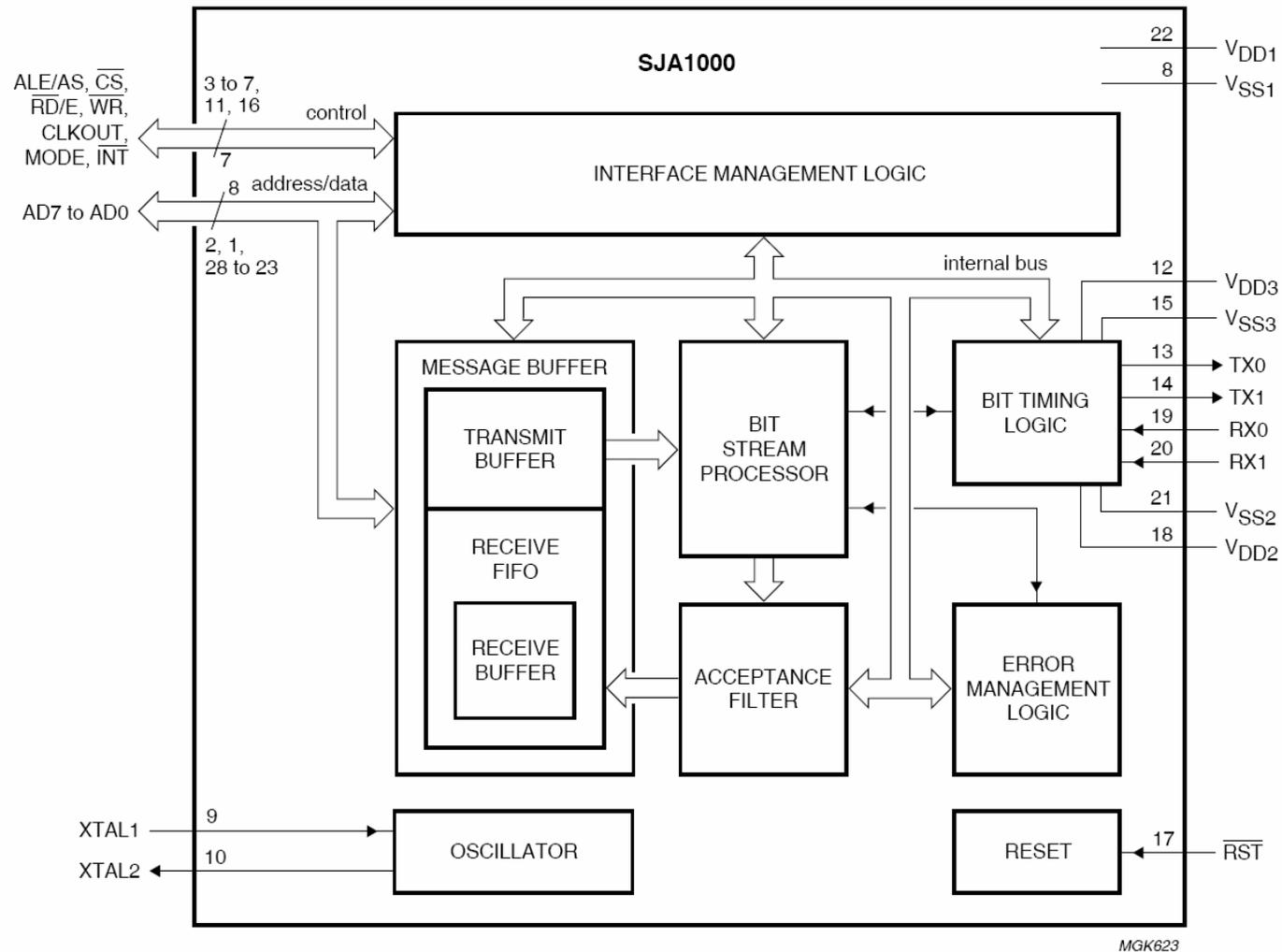


# Basic CAN Controller

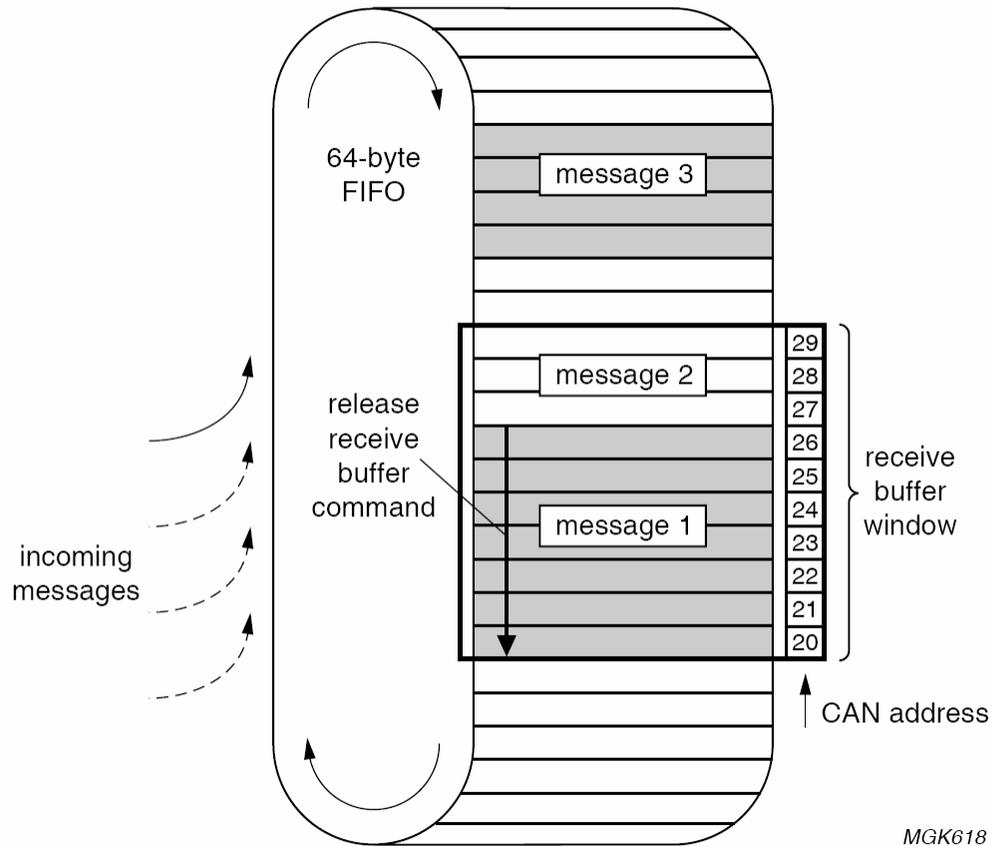
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# SJA1000 (Philips)

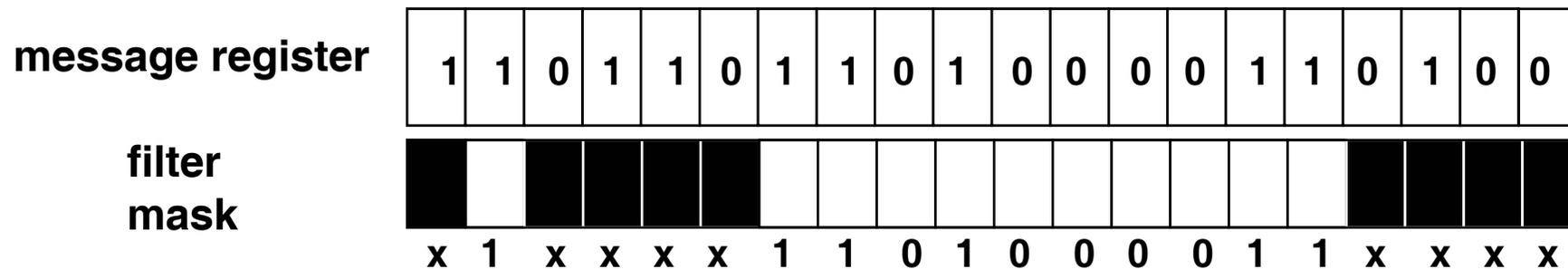


# SJA1000 (Philips)



# acceptance filtering

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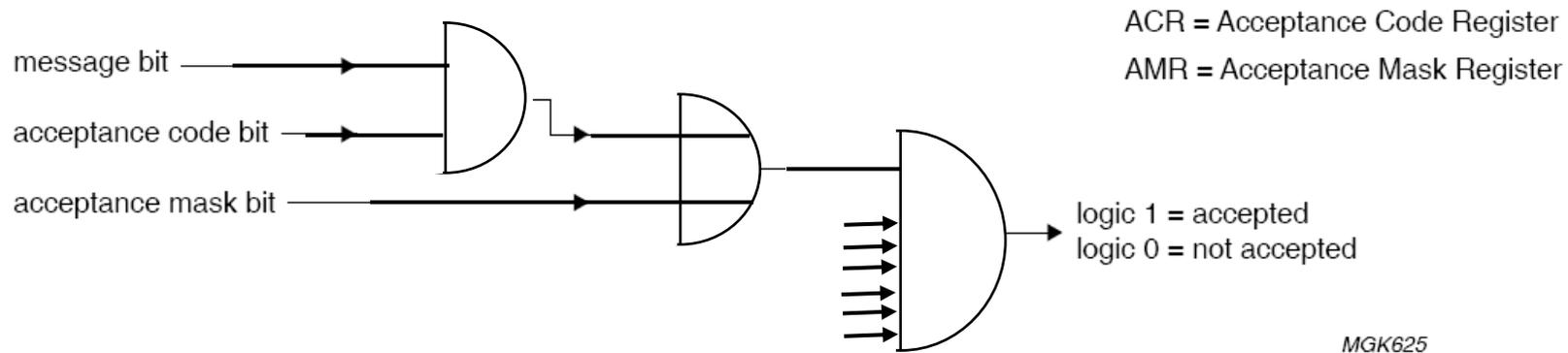
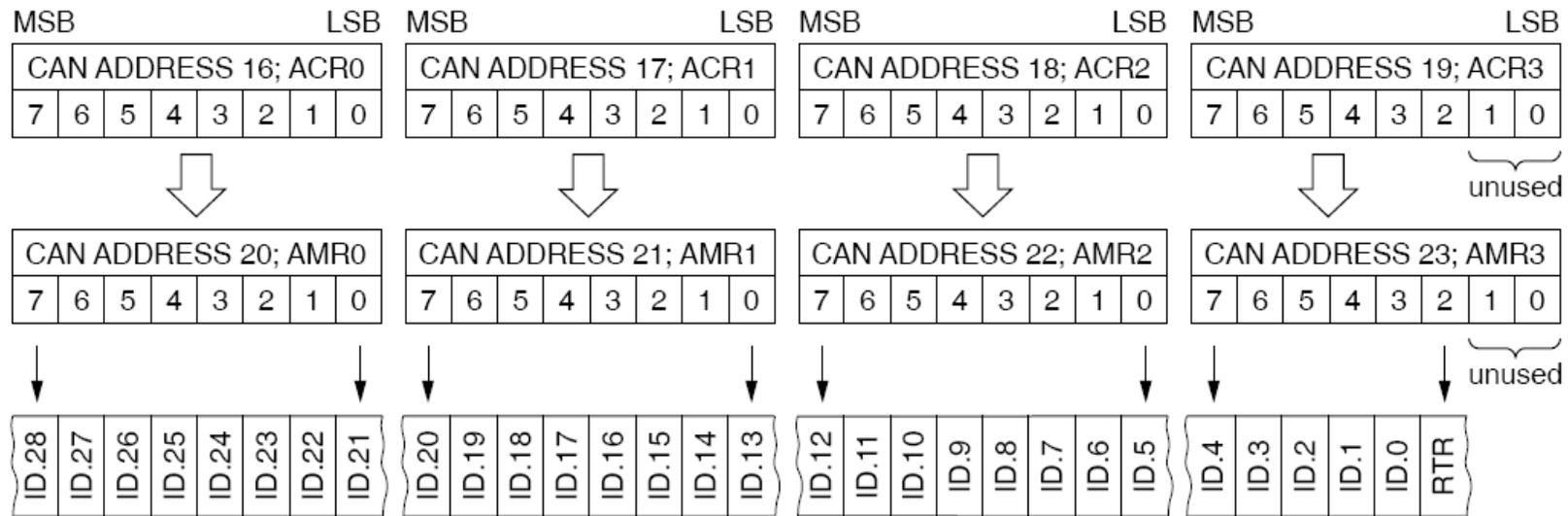


The number of message registers, configuration options and filters depend on the respective communication controller.



# SJA1000 (Philips)

## single mask option

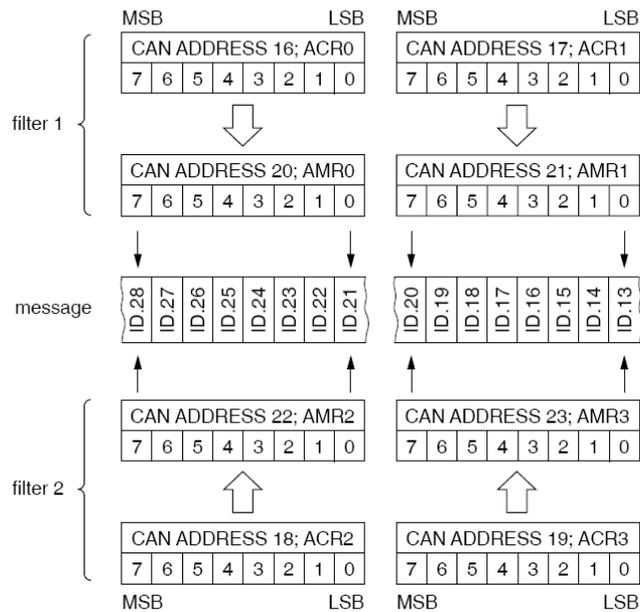


**ACR: defines the pattern of CAN message IDs which are accepted.**  
**AMR: defines a mask of "don't care" positions.**

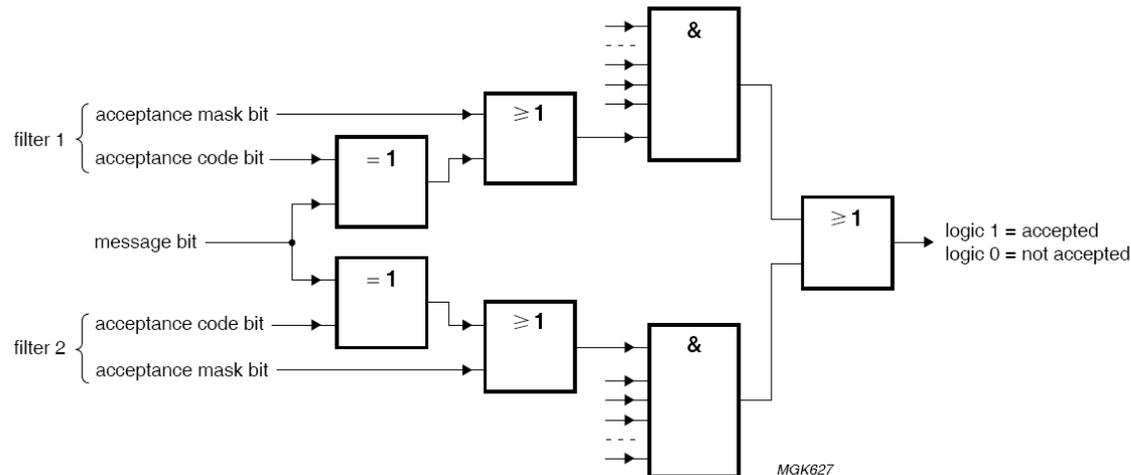


# SJA1000 (Philips)

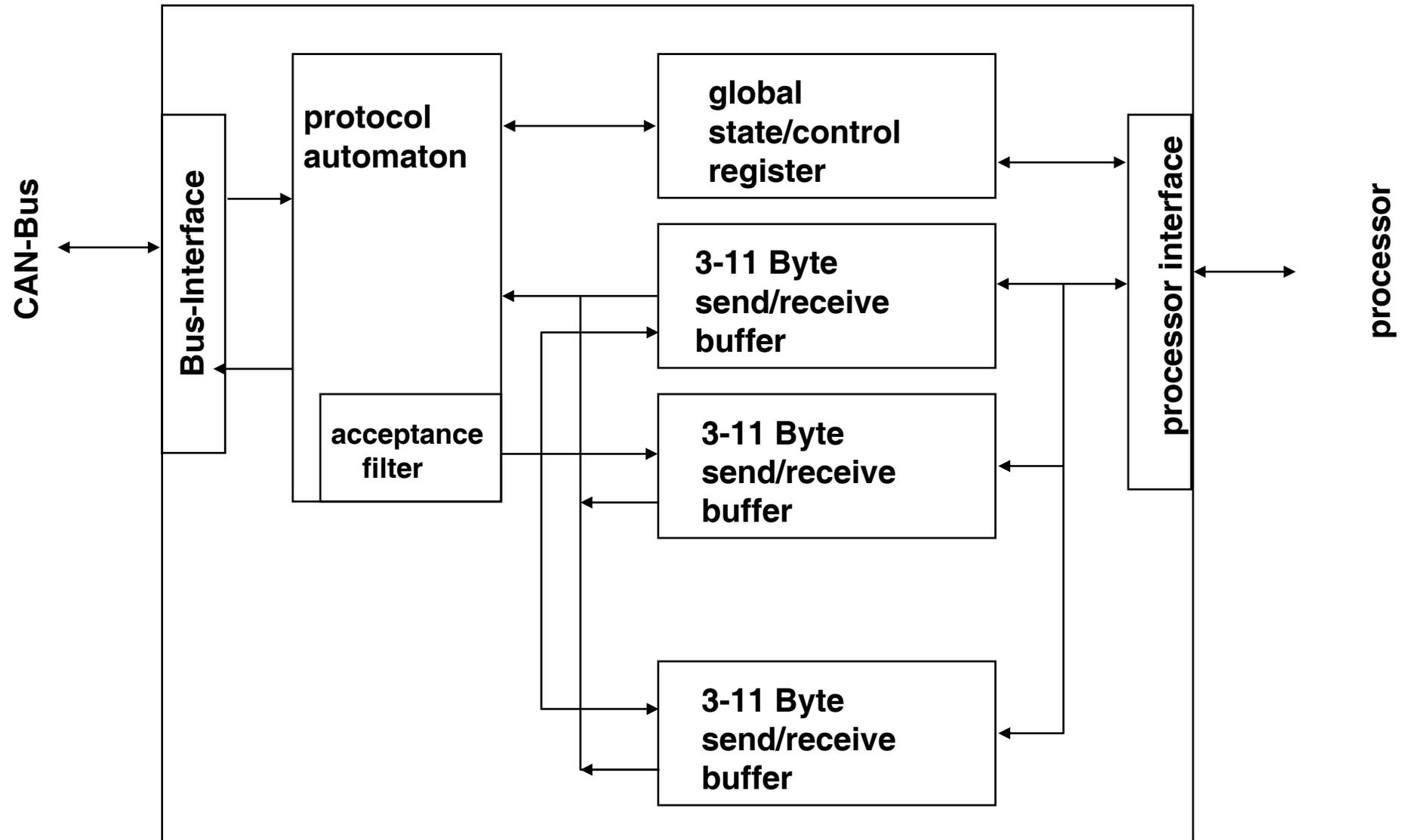
## dual mask option



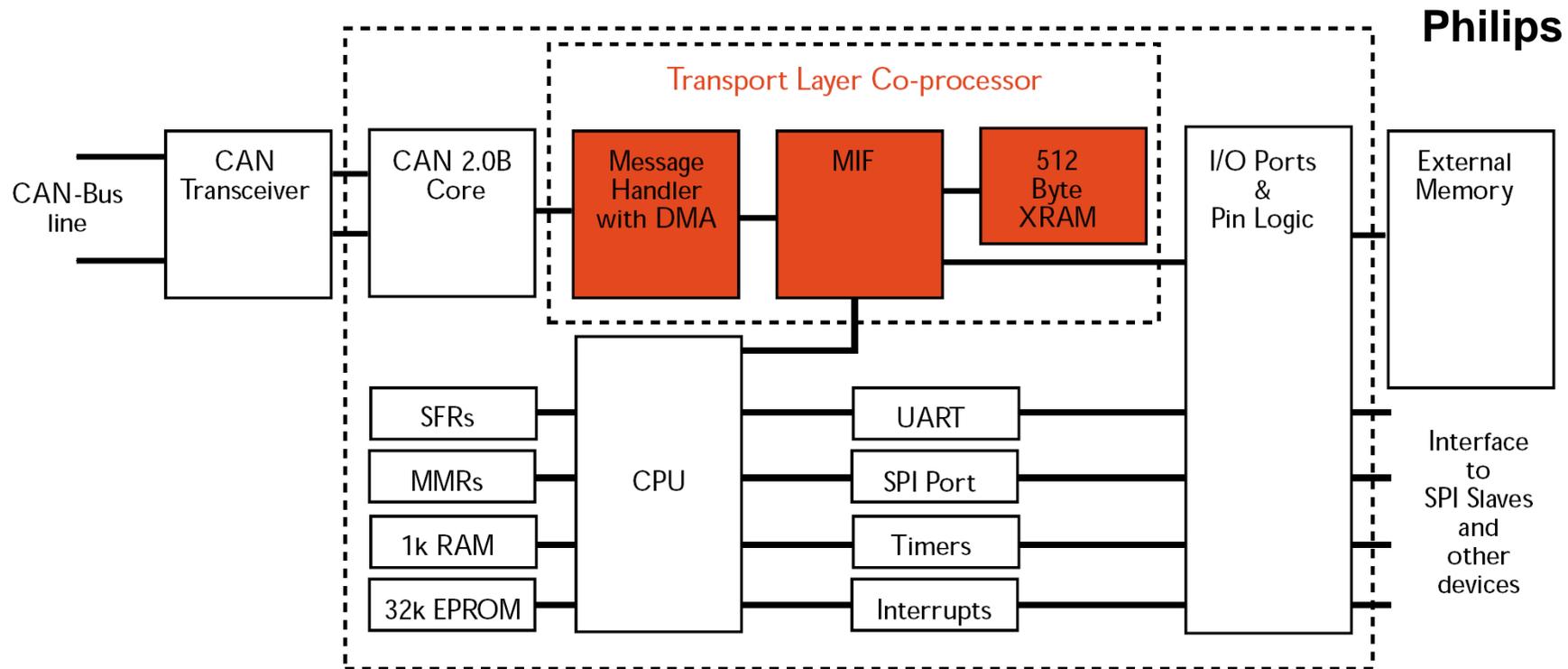
ACR = Acceptance Code Register  
AMR = Acceptance Mask Register



# Full CAN controller



# XA-C3: Support for higher layer protocols



## XA-C3 supports:

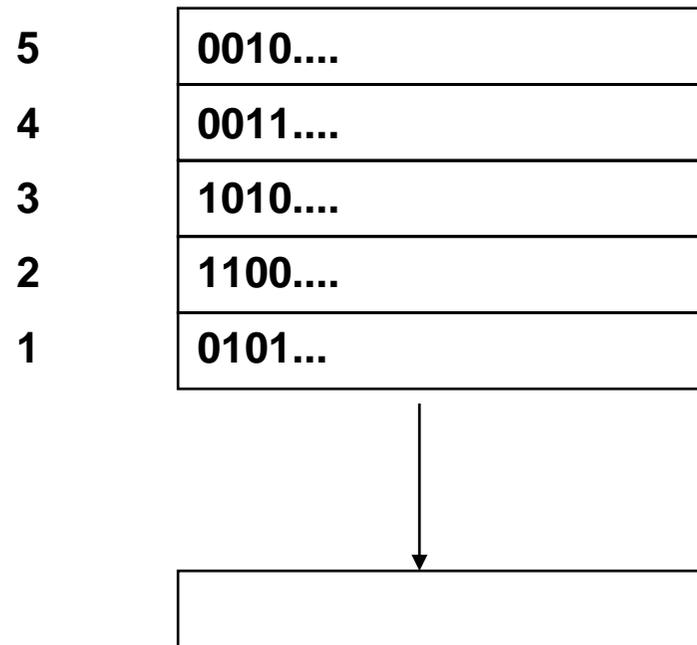
- Full CAN with 32 message objects
- Extended Buffering of received messages
- Pre-arbitration of local transmit-queue
- Fragmentation protocols according to various CAN Higher Level Protocols



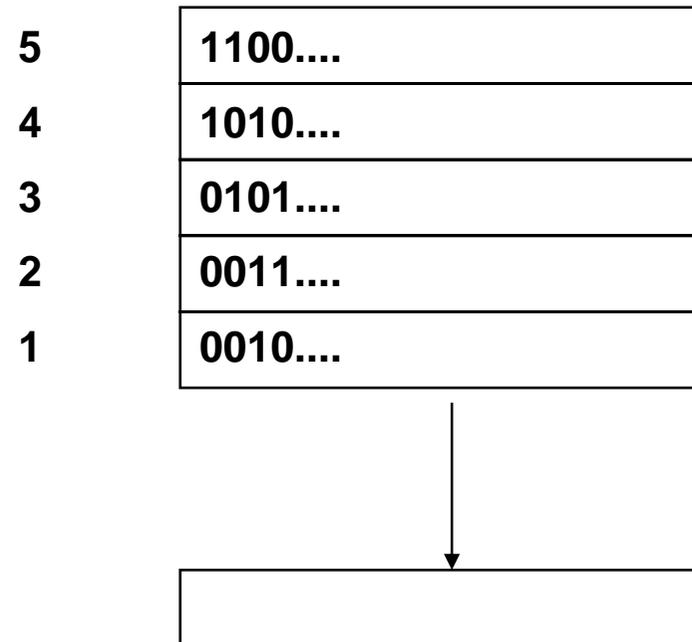
# Pre-arbitration in the local transmit queue

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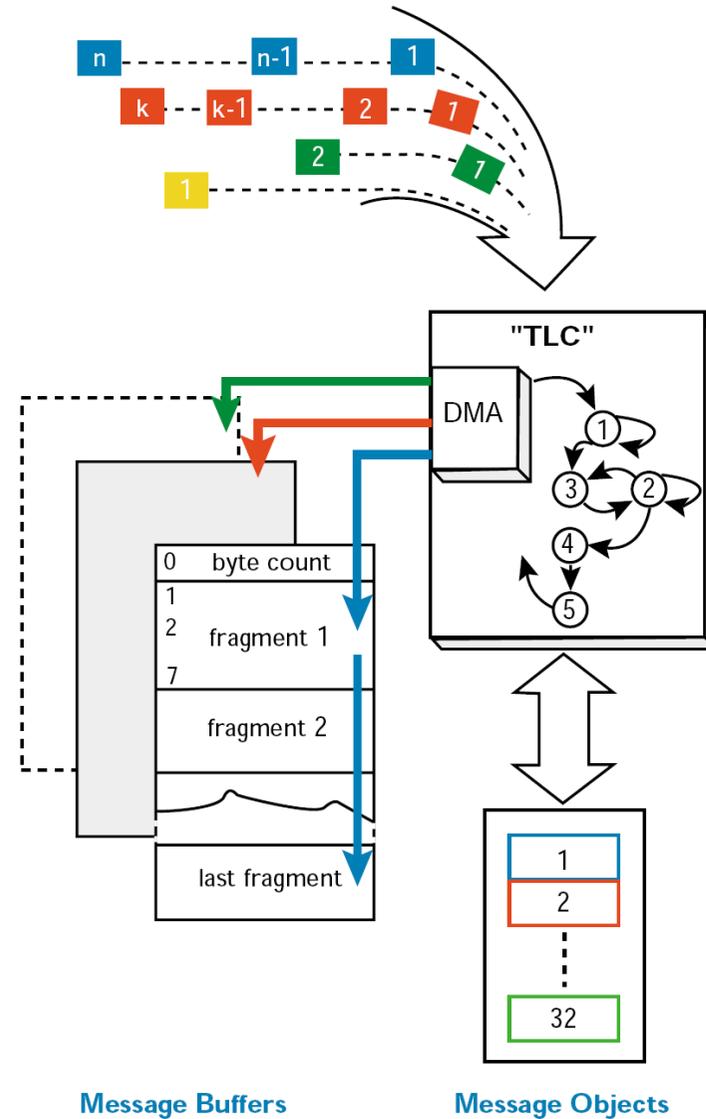
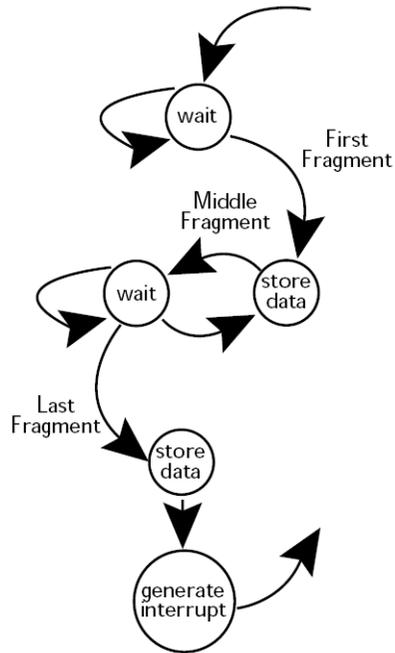
## Arbitration by message object #



## Arbitration by priority of message object



# Fragmentation Support



- Automatic reassembling of long messages.
- Supports CanOpen, Devicenet and OSEK standards



# What CAN can't

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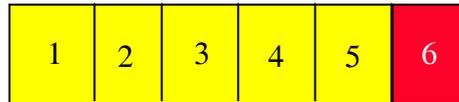
- **All-or-nothing property under all single (crash/omission) fault conditions**
- **Temporal guarantees for message transmissions**
- **Consistent order of messages**



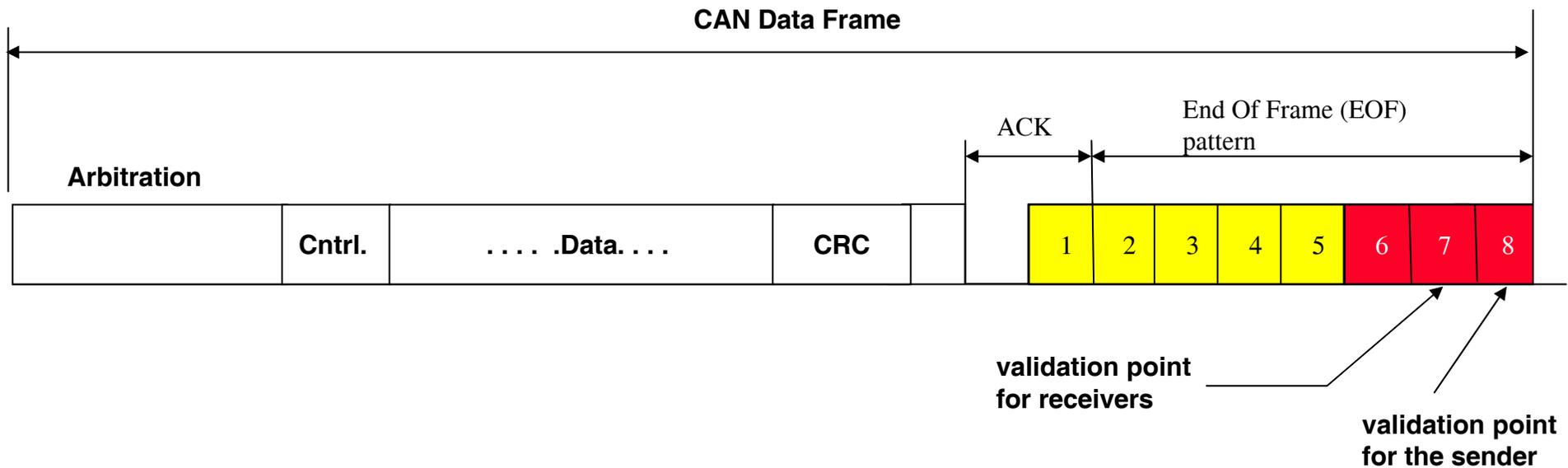
# Error Detection and Error Signalling in CAN

## The Case for Inconsistencies

Violation of the Bit-Stuffing Rule:  
Used for Error Detection and Signalling



Bit-Stuffing enforces the following rule:  
A sequence of 5 identical bit levels  
is followed by a complementary bit level



# Consequences from the validation protocol

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**J. Rufino, P. Veríssimo, C. Almeida , L. Rodrigues: „Fault-Tolerant Broadcasts in CAN“,  
*Proc. FTCS-28, Munich, Germany, June 1998.***

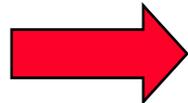
**J. Kaiser, Mohammad Ali Livani: “Achieving Fault-Tolerant Ordered Broadcasts in CAN”  
*Proc. of the 3<sup>rd</sup> European Dependable Computing Conference, (EDCC-3), Prague, Sept. 1999***



**inkonsistent message duplicates**



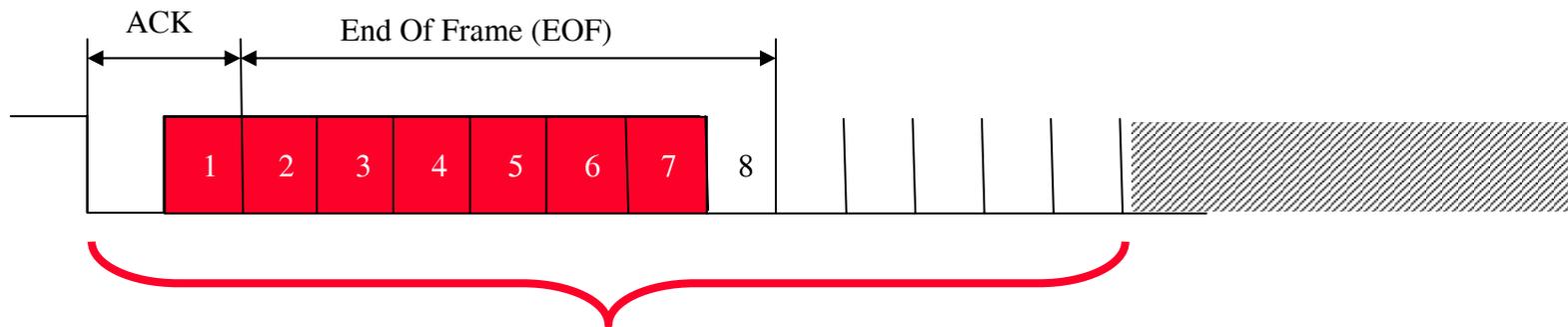
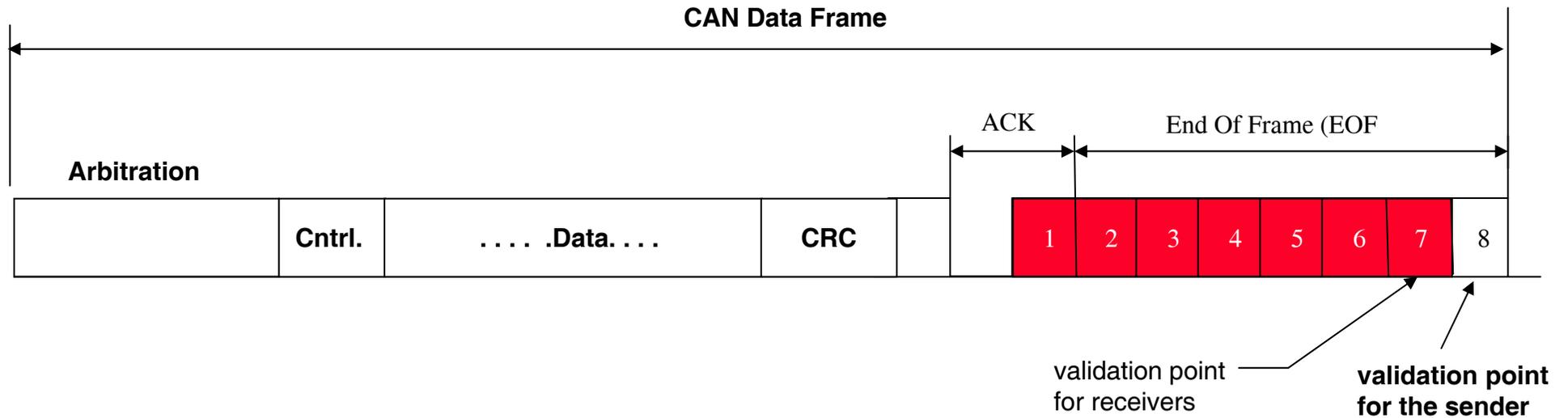
**inkonsistente omissions**



**(potentially) unbounded delays**



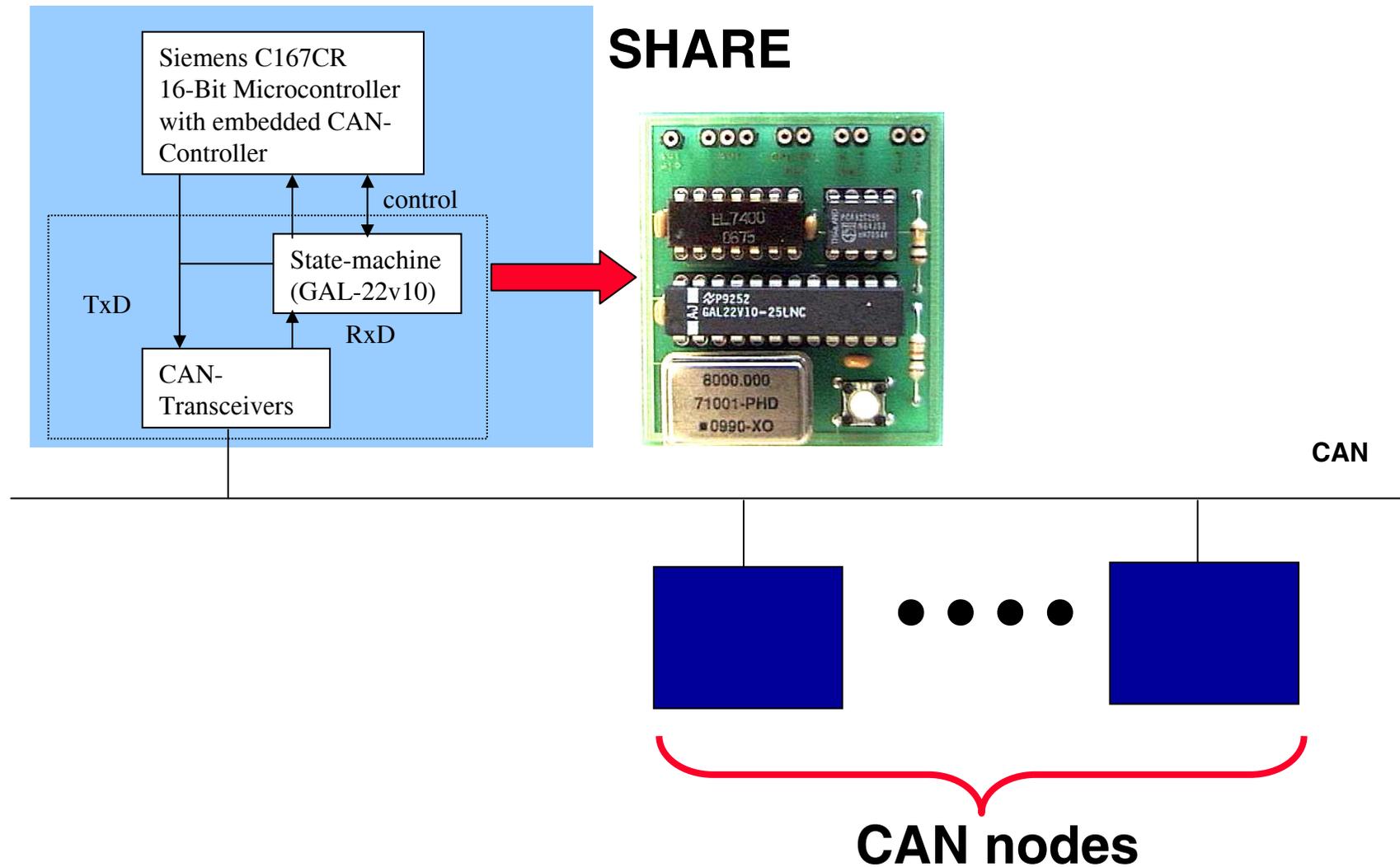
# The Case for SHARE: Inconsistent Omissions



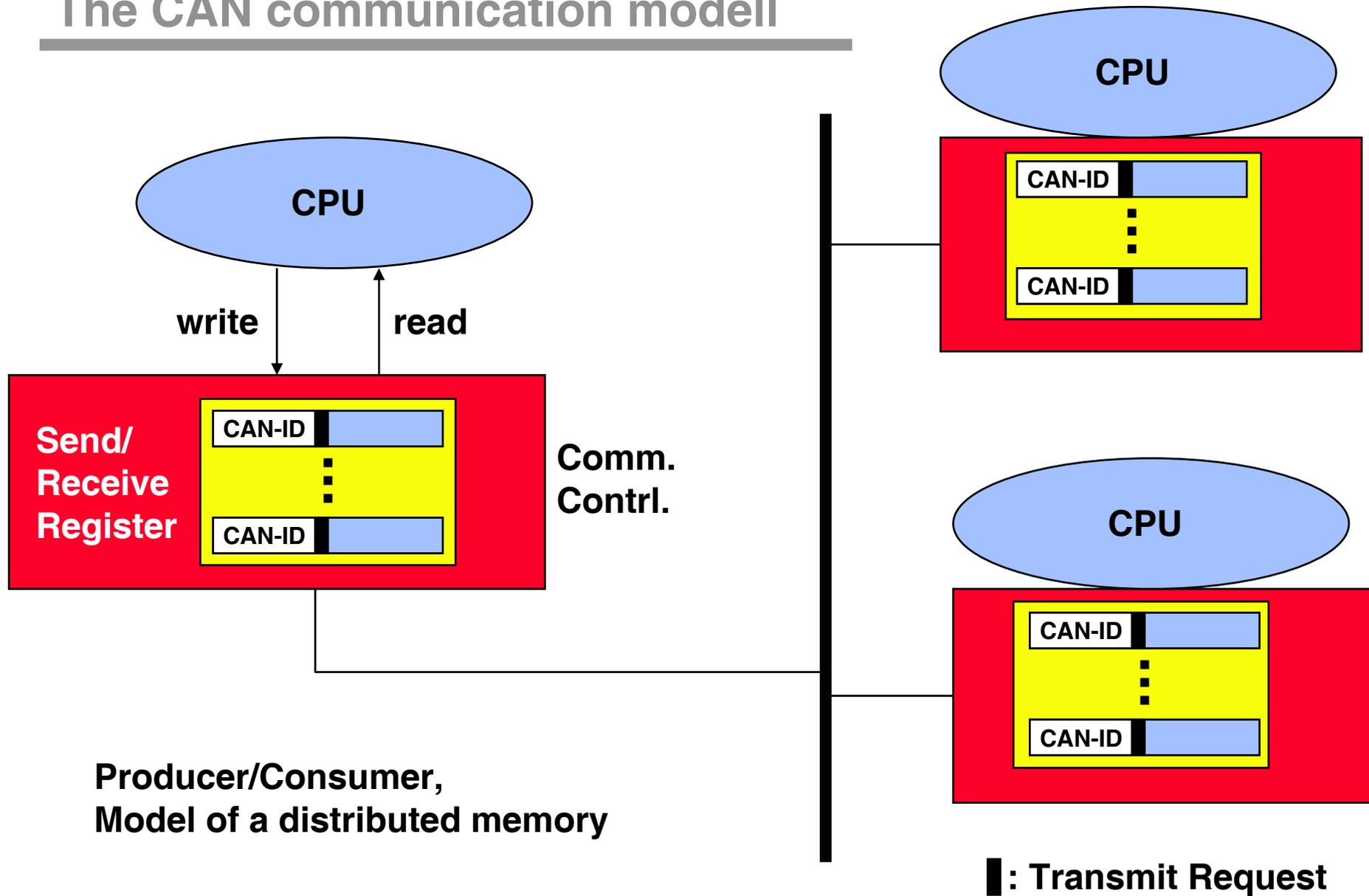
**unique pattern : 1 dominant, 7 recessive, 6 dominant !**



# The Architecture of SHARE



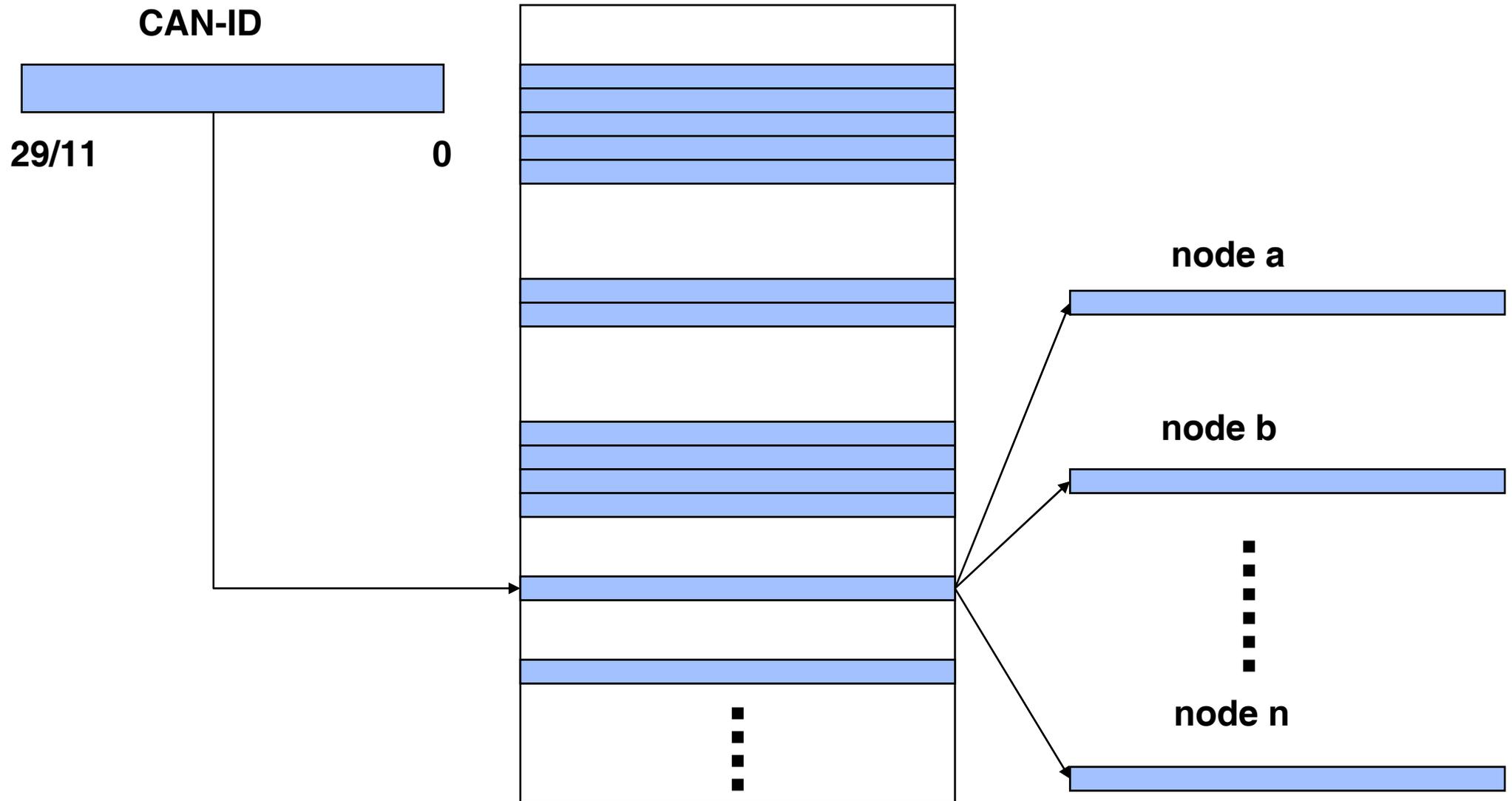
# The CAN communication modell



**Producer/Consumer,  
Model of a distributed memory**

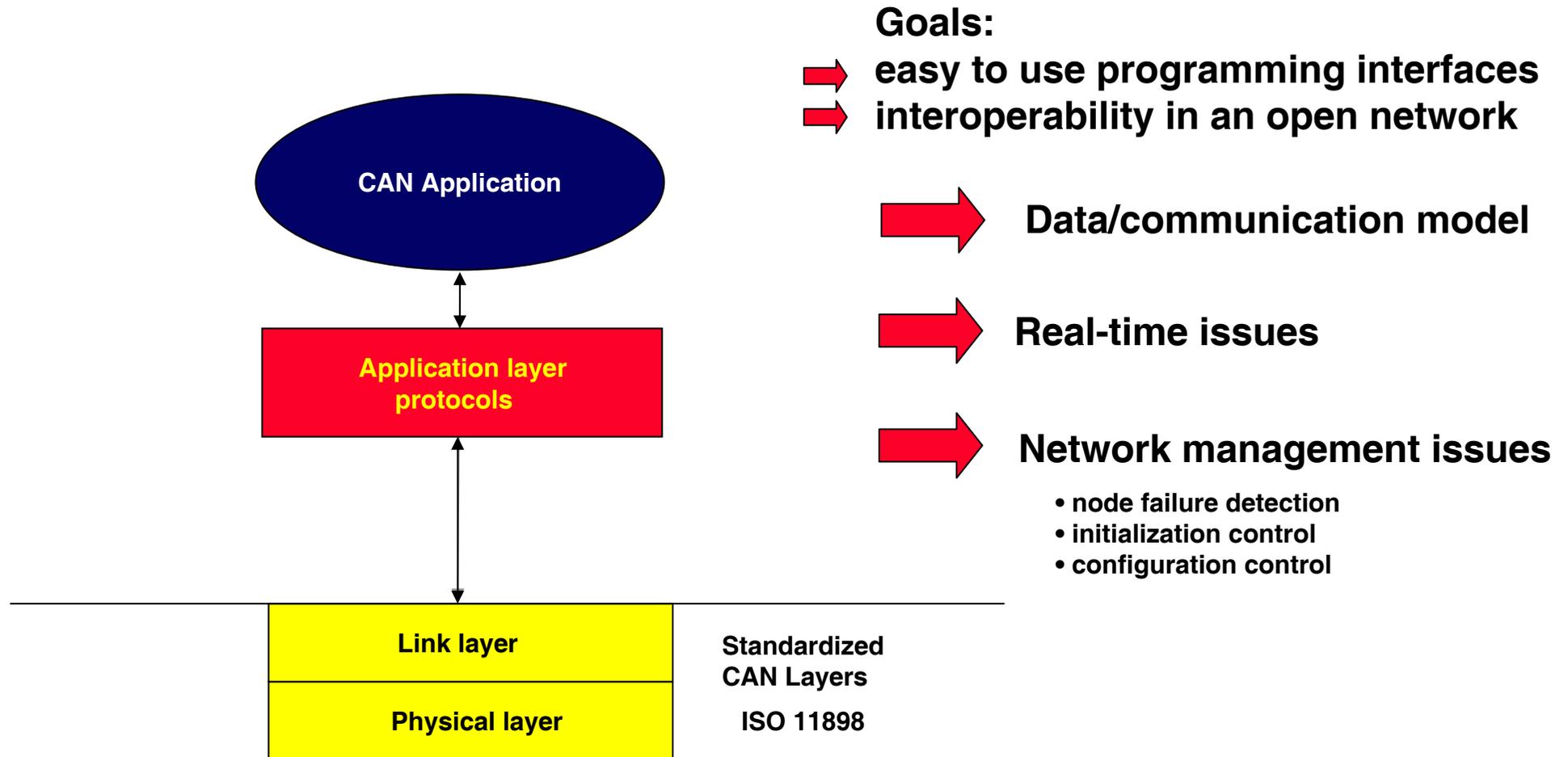


# The CAN communication modell



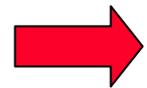
# Application Level Protocols

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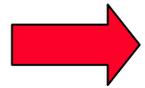
# Application Level Protocols

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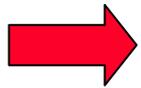
**CANopen**

**CiA**



**SDS: Smart Device Systems**

**Honeywell**

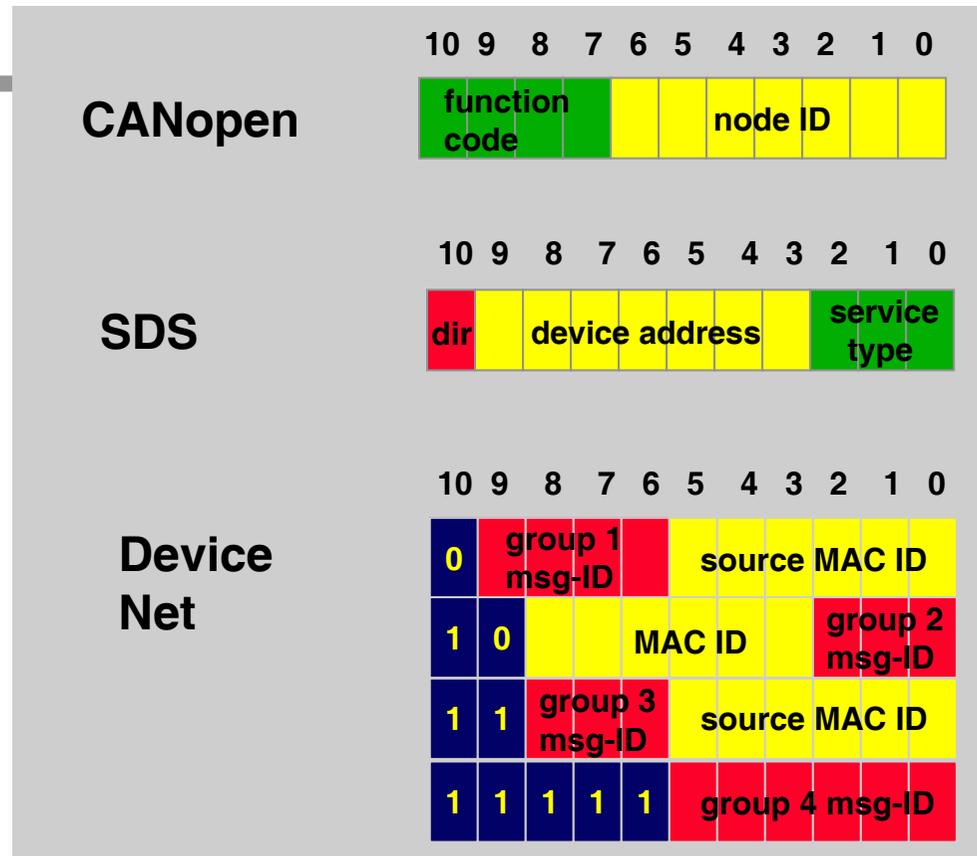


**Device Net**

**Allen Bradley**



# CAN-ID Assignment



	#nodes	# ID:	# of free prio.:	prio. determined by:
<b>CANopen</b>	128	16/node	0	functions
<b>SDS</b>	128	16/node	0	direction/device
<b>Device Net</b>	64	32/node	32	message groups



# Real-Time Support

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## Problem:

**Priorities of messages are not orthogonal to issues of routing or service specification**

Mechanism	CANopen	SDS	Device Net
restricted repetition rate	inhibit times	-	-
synch. by master	sync. Message	-	Master I/O Poll/Bit strobe
clock sync.protocol	high resolution	-	-
Bounds on service exec.		yes	



# MAC-protocols

controlled access

random access

**Collision avoidance**

**Collision resolution**

Reservation-based

Token-based

Time-based

Master-Slave

Priority-based

probabilistic

dynamic

static

ATM

TDMA:

TTP,  
Maruti

Token-Ring  
Token-Bus

Timed  
Token  
Protocol

CSMA/CA :  
Collision Avoidance

IEEE 802.11  
P-persistent CSMA

LON, VTCSMA

ProfiBus DP  
FIP  
CAN-Open

CSMA/CA :  
Consistent Arbitration

CAN

CSMA/CD :  
Carrier Sense Multiple Access /  
Collision Detection

Ethernet

